

2012 CANDE Workshop Program

March 16, 2012

Dresden, Germany

Workshop Program: Talks are 35 mins long, (approx. 25+ mins talk and 10- mins Q & A).
Keynote talks ~55 mins. (45 + 10 min Q + A).

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| 0835 | Opening Remarks & Intro to CANDE |
| 0845 | Session I: Topic area: System Design and Verification 0845 “25 Years Digital Design Mainstream using RTL Synthesis – Will there be additional 25 Years?” Speaker: Wolfgang Ecker, Infineon, DE 0920 “Is P = NP in the Cloud – Are we Designing the Right Verification Algorithms?” Speaker: Rolf Drechsler, U Bremen, DE |
| 0955 | Session II: Automotive Systems: Can EDA Help, or Not? 0955 “Is there Life Beyond the Chip Borders? An Automotive System Perspective” Speaker: Georg Pelz, Infineon, DE |
| 1030 | Coffee Break |
| 1100 | Session III: Keynote Talk I 1100 “System-Level Design: What are the Roadblocks and Opportunities?” Speaker: Alberto Sangiovanni Vincentelli, U California, Berkeley, US |
| 1155 | Lunch Break |
| 1300 | Session IV: Keynote Talk II 1300 CMOS Reliability: Analysis and Optimization Speaker: Sachin Sapatnekar, U Minnesota, US |

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| 1355 | <p>Session V: Next Generation Lithography Techniques: Are you ready?</p> <p>1355 “EDA View on Next Generation Lithography Techniques”</p> <p>Speaker: Kostas Adam, Mentor Graphics, US</p> |
| 1430 | <p>Coffee Break</p> |
| 1500 | <p>Session V Contd.</p> <p>15:00 “Lithograph Challenges and the Impact on Design-Flow under 22nm Technology Node”</p> <p>Speaker: Gi-Joon Nam, IBM, US</p> |
| 1535 | <p>Session VI: Embedded Systems</p> <p>1535 “Dependable Embedded Software for Undependable Hardware”</p> <p>Speaker: Joerg Henkel, Karlsruhe Institute of Technology, DE</p> <p>1610 “Requirements and Tools for Embedded Platform Mapping in Communication Systems”</p> <p>Praveen Raghavan, IMEC, BE</p> |