CANDE 2002

IEEE Circuits and Systems Society

September 22-24, 2002

Alyeska Prince Resort - near Anchorage, Alaska

Sunday, September 22

6:30 - 9:00 PM   Opening Reception

Monday, September 23

7:30 - 8:30   Breakfast

8:30 - 11:30   How does EDA deal with embedded S/W & H/W-dependent S/W
                   (Organizer: Grant Martin)

   EDA will lead with tools, flows and methods for embedded software/HdS
   Mike Kaskowitz, Mentor Graphics
   The embedded software tools industry will lead; EDA's contribution is marginal
   Bruce Powel Douglass, iLogix
   A Dose of Reality: The tools, flows and methods that are really needed
   Colin Tattersall, Beach Solutions

10:00 - 10:30   Session Break

11:30 - 1:00   Lunch

1:00 - 2:30   Asynchronous Design Issues (organizer: Vassilios Gerousis)

   An introduction to the GALS methodology at ETH Zurich
   Frank Gürkaynak, Integrated Systems Lab – Swiss Federal Inst. of Tech.
   Self Timed Asynchronous Design
   Doug Edwards, University of Manchester

2:30 - 6:00   Individual Discussions and Outside Activities

6:00 - 7:00   Wine and beer social hour

7:00 - 9:00   Dinner & Speakers (Organizer: Ian Getreu)

   On-chip Optical Interconnect
   Jaijeet Roychowdhury, University of Minnesota

Tuesday, September 24

7:30 - 8:30	Breakfast
8:30 - 11:30	Experiences Using Design IP (Organizer: Mark Flomenhoft)

Why Using Third Party IP Keeps Me Awake at Night
Michael Solka, Synopsys Professional Services
IP assembly in a 100M transistor VoP device
Mojy Chian, Mindspeed Technologies
The Next Step in IP-Reuse
Ramesh Chandra, ST Microelectronics

10:00 - 10:30	Session Break
11:30 - 1:30	Lunch and CANDE meeting