

## IEEE CAS Power Systems and Power Electronic Circuits Technical Committee

<http://ewh.ieee.org/cmte/cas-pspec>

May 19, 2008, Time: 12:50 – 14:00

Meeting Minutes:

The Power Systems and Power Electronic Circuits Technical Committee (PSPEC-TC) met in the Greenwood room of the Sheraton Seattle Hotel on May 19, 2008, during ISCAS'08, Seattle, WA, USA.

### 1. Introduction

The committee Chair Miroslav Begovic could not be present at the meeting due to complications of his traveling schedule to Shanghai. Fortunately, he was able to meet with past Chair Eduard Alarcon in Atlanta GA few days prior to the ISCAS 2008 and discuss the meeting details. Therefore, the meeting was chaired by Eduard Alarcon on behalf of Miroslav Begovic. Eduard Alarcon opened the meeting at 12:50, during the lunch, and welcomed the 9 people who attended the meeting. All attendees made brief introductions. The attendance signup sheet, meeting agenda, and the minutes of the ISCAS 2007 meeting in New Orleans were circulated.

### 2. Chair's Report (June 2007 – May 2008)

Eduard Alarcon presented annual Technical Committee's activity report.

- a) The IEEE Transactions on Circuits and Systems, Part I, Regular Papers (**TCAS-I**), and IEEE Transactions on Circuits and Systems, Part II (**TCAS-II**) have recently experienced a change of Editors-In-Chief (EIC). Presently, the Editor-In-Chief of **TCAS-I** is Gianluca Setti, and Editor-In-Chief of **TCAS-II** is Un-Ku Moon. Both EICs are very actively trying to clear out the backlog of papers and the exact numbers of papers that belong to PSPEC committee were not available before the meeting. The Associate Editors (AEs) currently representing our committee in TCAS-I are Henry Chung, Eduard Alarcon, Karen Miu, Chi K. Tse (Michael), and partially Philip Mok, Volkan Kursun and Mario di Bernardo. The Associate Editors (AEs) currently representing our committee in TCAS-II are Chika Nwankpa, Gabriel A. Rincón-Mora.
- b) IEEE International Symposium on Circuits and Systems, 2008, Seattle.
  - i) The number of submitted papers was 63, and the number of accepted papers was 30, which resulted in acceptance rate of about (45%). Overall we had 2 poster sessions on Tuesday May 20th with 5 papers in each, and 4 oral sessions on Wednesday May 21st with 5 papers in each session.
  - ii) Special session: No special sessions were proposed this year.
  - iii) Tutorials: One tutorial was proposed by Juri Jatskevich on Average-Value Modeling of Power Electronic Circuits and Systems. The tutorial was canceled due to low enrolment. Tutorials were scheduled for May 18th, whereas the day in which our track had the most sessions and papers was May 21st. Unfortunately many people who are active within our committee had traveling constraints and could not make it for the May 18 to attend the tutorial.
- c) Other CAS-Sponsored Conferences:
  - i) No reports from other CAS-sponsored conferences with PSPEC track were available for 2008.

- ii) IEEE International Midwest Symposium on Circuits and Systems (MWSCAS 2007) took place in Montreal, Canada, August 5 – 8, 2007. The PSPEC track was chaired by Eduard Alarcon and Praveen Jain with the total of 15 papers.
- iii) European Conference on Circuit Theory and Design (ECCTD'07), took place in Seville, Spain, from August 26 to August 30, 2007. Eduard Alarcon was a Technical Program Co-Chair. Dariusz Czarkowski chaired the track on Power Circuits and Systems with the total of about 10 papers.

### 3. Proposed Activities for 2008/09, Chair/Elect – Miroslav Begovic / Tadashi Suetsugu

- a) ISCAS'09 – May 24-27, 2009, Taipei, Taiwan. The call for papers is already available on the web (<http://conf.ncku.edu.tw/iscas2009>). The deadline for both is September 13, 2008. The deadline for submitting full 4-page regular papers in October 18, 2008. Since these deadlines are fast approaching, the members of PSPEC-TC are reminded and encouraged to submit proposals for tutorials and special sessions, as well as spread the word to their colleagues who are working in related areas. It was pointed out that we should more actively recruit authors for ISCAS, especially for power systems sub-track. The desirable net result should be more accepted papers within our committee, and better quality (uniformity of theme) for lecture sessions especially.
- b) People are reminded that all proposals for special sessions and tutorials should be first endorsed by the PSPEC-TC chair. However, the actual submission of proposals is done through the regular website of ISCAS and the planning committee. In the final submission, the author(s) should clearly indicate that the proposal has been endorsed by the PSPEC-TC.
- c) The theme of ISCAS'09 is “**Circuits and Systems for Human Centric Smart Living Technologies.**” So, there will be opportunities to propose sessions related to power circuits in wear/mobile devices, on-chip power, as well as smart power technologies.
- d) ISCAS'09 will be trying to launch a new track that would include various demos. So, there may be an opportunity to demonstrate some hardware and/or applications that could potentially attract many people.
- e) CAS is running a sponsored Distinguished Lecture Program (DLP), which is composed on up to 20 speakers from different areas within CAS activities, 10 of which are rotated each year. The PSPEC-TC members are encouraged to nominate the new DLP speakers for the next year. The criterion for outstanding speaker include: timeliness of the topic, relation to industry, breadth, and appeal to a larger CAS audience.

### 4. Approval of the 2007 Meeting Minutes

During the meeting, attendees had an additional opportunity to review the minutes. Secretary Juri Jatskevich has moved to approve the minutes. The motion was seconded and approved unanimously.

### 5. Election of Chair-Elect

PSPEC-TC has a policy of alternating chairmanship between the power electronics and power system communities. Since the incoming Chair Tadashi Suetsugu from Fukuoka University in Japan is mostly associated with power electronic circuits, an election was held to determine the Chair-Elect from the power systems and circuits area for the duties

during the ISCAS'10. Juri Jatskevich from the University of British Columbia, Vancouver, Canada, was the sole nomination. Eduard Alarcon has moved to approve the nomination. The motion was seconded and approved unanimously.

## 6. Election of Secretary

PSPEC-TC secretary typically serves for two years. Juri Jatskevich has served the ISCAS 2007 and 2008 terms, taking the turn from Tadashi Suetsugu who served the ISCAS 2005 and 2006. After some discussion during the meeting, Tadashi Suetsugu nominated Hirotaka Kozumi from the Tokyo Univ. of Science, Japan. The motion was seconded and approved unanimously.

## 7. Meeting Adjournment

Meeting was adjourned at 14:00, after which many people went back to sessions.

Minutes taken by  
Juri Jatskevich,  
Secretary of PSPEC-TC

CAS PSPEC, ISCAS 2008, Attendees List

	Name	Affiliation	Nation	E-mail
1	Tsorng-Juu (Peter) Liang	National Cheng Kung University	Taiwan	tjliang@mail.ncku.edu.tw
2	Ali Davoudi	Univ. of Illinois at UC	USA	davoudi2@uiuc.edu
3	Tadashi Suetsugu	Fukuoka Univ.	Japan	suetsugu@fukuoka-u.ac.jp
4	Hirotaka Kozumi	Tokyo Univ. of Science	Japan	littlespring@ieee.org
5	Hiroo Sekiya	Chiba University	Japan	sekiya@faculty.chiba-u.jp
6	Guoping Liu	Washington State University, Pullman	USA	gliu@eecs.wsu.edu
7	Eduard Alarcon	UPC	Spain	talarcon@eel.upc.edu
8	Juri Jatskevich	Univ. of British Columbia	Canada	jurij@ece.ubc.ca