

EDTM2020 Preliminary Conference Agenda

As of January 29, 2020

Day 0 - Sunday, March 15

Session Time	Grand BR I	Grand BR III	Bayan Room
08:00-12:30	Tutorial 1	Tutorial 2	Tutorial 3
12:30-13:30	Lunch Break		
13:30-17:30	Short Course 1	Short Course 2	

Day 1 - Monday, March 16

Main Room (Matahari I, II, III)					
Session Time	Room A Matahari I	Matahari II	Room B Matahari III	Room C Grand BR I	Room D Grand BR III
08:00-08:30	Opening Remarks				
08:30-09:15	Plenary 1				
09:15-10:00	Plenary 2				
10:00-10:45	Plenary 3				
10:45-11:15	Exhibitors Presentation				
11:15-12:15	Lunch Break (Rooms Etoile and Grand BR II)				
	1A		1B	1C	1D
12:15-14:20	Focus Session 1: Thin-Film Transistor		Nanotechnology Materials and Manufacturing	Density Functional Theory-Based Simulation	Power and RF Devices
14:20-14:40	Authors' Interview / Poster Viewing / Coffee Break				
	2A		2B	2C	2D
14:40-16:45	GaN and III-V Devices		Large Area and Flexible Electronics	TCAD-1	Design and Technology
16:45-17:05	Authors' Interview / Poster Viewing / Coffee Break				
	3A		3B	3C	3D
17:05-18:45	GaN Power Devices		2.5/3D Integration	TCAD-2	Advanced Photovoltaic Devices and Detectors
18:45-19:00	Authors' Interview				
19:00-21:30	Poolside Reception@Equatorial				

Day 2 - Tuesday, March 17

Main Room (Matahari I, II, III)					
Session Time	Room A Matahari I	Matahari II	Room B Matahari III	Room C Grand BR I	Room D Grand BR III
07:30-08:30				YP & WiE Event	
08:30-09:15	Plenary 4				
09:15-10:00	Plenary 5				
10:00-10:15	Short Break				
10:15-12:20	4M Focus Session 2: Reliability and DTCO			4C Novel Photodetectors	4D Materials for Novel Devices
12:20-13:20	Authors' Interview / Lunch Break (Rooms Etoile and Grand BR II)				
13:20-15:00	5A Reliability 1		5B Compact Modeling	5C Sensors and Inductors	5D Materials for Manufacturing
15:00-15:20	Authors' Interview / Poster Viewing / Coffee Break				
15:20-17:00	6A Reliability 2		6B Memory Modeling and Interface Trap Modeling	6C Neural Network and NVM 1	6D SiC-Power Devices
17:00-17:15	Authors' Interview				

Day 3 - Wednesday, March 18

Session Time	Room A Matahari I	Room B Matahari III	Room C Grand BR I	Room D Grand BR III
08:30-09:15	Plenary 6			
09:15-10:00	Plenary 7			
10:00-10:15	Short Break			
10:15-12:20	7A Variability Modeling	7B Advanced Transistors	7C Materials Processing	7D 2D Materials and Devices
12:20-13:20	Authors' Interview / Lunch Break (Rooms Etoile and Grand BR II)			
13:20-15:00	8A Neural Network and NVM 2	8B Manufacturing and Characterization	8C Emerging Memory for IoT	8D Focus Session 3: Packaging and Heterogeneous Integration
15:00-15:20	Authors' Interview / Coffee Break			
15:20-17:00	JSAP (JST / CREST)	9B RF Device Modeling	Empty	Heterogeneous HRI Road Map Group WS
17:00-17:20		Authors' Interview		
15:20-19:20		Empty	Empty	Empty