



IEEE Green Mountain Section (GMS) Awards

recognizes outstanding contributions to the advancement of the engineering profession


2011 IEEE GMS Award Recipients

1. IEEE GMS “**ENGINEER OF THE YEAR**” AWARD – Sponsored by the IEEE Green Mountain Solid-State Society Chapter


 *John E. Barth Jr.*, Distinguish Engineer, IBM

→ “for seminal improvements to the state of the art of Embedded Dynamic Random Memory Circuit Technology”

2. IEEE GMS “**LEADERSHIP**” AWARD – Sponsored by the IEEE Green Mountain Section

 *Dawn Densmore*, Director Outreach and Public Relations, University of Vermont

→ “for seminal improvements to the state of the art of Science, Technology, Engineering and Mathematics Education”

 *Tian Xia*, Associate Professor, Electrical and Computer Engineering, School of Engineering, University of Vermont

→ “for seminal improvements to the state of the art of Circuit Design and Test Technology”

3. IEEE GMS “**ENTREPRENEUR OF THE YEAR**” AWARD – Sponsored by the IEEE Green Mountain Section

 *Mike Slattery*, President/Engineer, asicNorth


→ “for seminal improvements to the state of the art of Circuit Technology and Services”

4. IEEE GMS “**TECHNOLOGY OF THE YEAR**” AWARD – Sponsored by the IEEE Green Mountain Solid-State Society Chapter

 *A. J. Rossman*, Founder, DRAKER LABORATORIES

→ “for seminal improvements to the state of the art of Photovoltaic Technology and Services”

5. IEEE GMS “**SERVICE OF THE YEAR**” AWARD – Sponsored by the IEEE Green Mountain Computer Society Chapter


 *David A. Hallquist*, CEO, Vermont Electric Cooperative

→ “for seminal improvements to the state of the art of Smart Grid Technology and Services”



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6. IEEE GMS **"FACULTY OF THE YEAR"** AWARD – Sponsored by the IEEE Green Mountain Section

 *Jeff Frolik*, CEMS, University of Vermont

→ "for seminal improvements to the state of the art of Multi-Disciplinary Engineering Education"

7. IEEE GMS **"OUTSTANDING TECHNICAL CONTRIBUTION"** AWARD – Sponsored by the IEEE Green Mountain Solid-State Society Chapter


 Yves T. Ngu, IBM

→ "for seminal improvements to the state of the art of Compact Modelling of Through-Silicon-Via (TSV) Technology"

 *Anthony K. Stamper*, Senior Technical Staff Member, IBM


→ "for seminal improvements to the state of the art of Micro-Electro-Mechanical Systems (MEMS) Technology"

8. IEEE GMS **"COLLEGE OF THE YEAR"** AWARD – Sponsored by the IEEE Green Mountain Section


 *Dr. Philip A. Conroy*, President, Vermont Technical College

→ "for seminal improvements to the state of the art of College Education"

9. IEEE GMS **"INNOVATION"** AWARD – Sponsored by the IEEE Green Mountain Solid-State Society Chapter

 *Geordie M. Braceras*, Distinguished Engineer, IBM


→ "for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development"

 *Jeanne P. Bickford*, Senior Technical Staff Member, IBM

→ "for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development"

 *Igor Arsovski*, IBM

→ "for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development"

 *Edward C. Cooney III*, IBM

→ "for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development"



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- *David L. Gardell*, Senior Engineer, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”
- *Paul D. Kartschoke*, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”
- *David E. Lackey*, Distinguished Engineer, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”
- *Junjun Li*, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”
- *Qizhi Liu*, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”
- *Douglas W. Stout*, IBM
 - “for seminal improvements to the state of the art of Semiconductor Intellectual Property Portfolio Development”