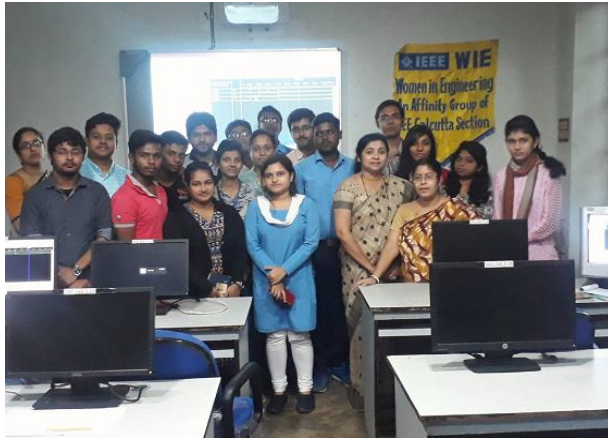## Report: WIE Event 5

**Event: Two-day Workshop on "FPGA for Digital Circuits & Systems Design"**
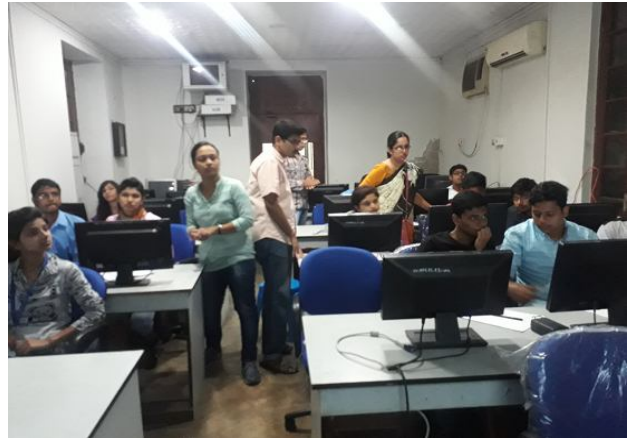**Date: November 16 & 17, 2017**
**Venue**: **VLSI Laboratory**, **Department of Radio Physics and Electronics (RPE), University of Calcutta**

A Two-day Workshop on **"FPGA for Digital Circuits & Systems Design"** was organized by **IEEE WIE, Kolkata Section**, at the **VLSI Laboratory** (Ground Floor) in the **Department of Radio Physics and Electronics (RPE), University of Calcutta** on **November 16 & 17, 2017.** Fifteen students (third year and final year) from the Camellia Institute of Technology participated in the said workshop. The inaugural program started at 11:00 a.m. In the inaugural session, Dr. Sudipta Chatterjee, Chairperson, WIE, emphasized on the bridging the gap between men and women engineers in the work fields. In this regard she mentioned the need for organizing such type of workshop under the banner of IEEE WIE. She had also given an overview about the entire workshop. The technical session started at 11:30 a.m. Dr. Arpita Das and Dr. Sumitra Mukhopadhyay, faculty members of the Dept. of Radio Physics and Electronics, University of Calcutta, acted as the resource persons for the said workshop. Dr. Das gave them a brief overview of FPGA. She explained the students about the steps to be followed to write any program in VHDL. The students followed the power point presentation and tried to do the same in their assigned systems. Four volunteers were assigned in the laboratory to help the students. Lunch was provided to the participants at 1:30 p.m. The post lunch session started at 2:30 p.m. In this session, the students were given simple assignments such as designing of basic and universal logic gates using Xylinx FPGA kit. All the students could do these assignments successfully. The first day of the workshop ended at 5:00 p.m. The second day of the workshop also started at 11:00 a.m. In this session, the students were given some design problems related to combinational digital circuits such as, Half adder, Full adder, Code converters, Decoders, Parity generator & Checker, Multiplexer, De-multiplexer etc. The participants successfully applied their acquired knowledge to solve the given problems. Resource persons and volunteers guided them all the time. In the valedictory session, the certificate of participation was handed over to each of the attendees. Oral feedback was also taken from the participants. They expressed their

eagerness to participate in the similar type of workshop in future. The feedback received from the students was really encouraging.



WIE members with the participants



Participants are solving the assignments in FPGA



Dr. Sanghamitra Chatterjee, Secretary,WIE, is distributing certificate to the participant



Dr. Sudipta Chatterjee, Chairperson, WIE, is distributing certificate to the participant