

April 2008See us on the WEB at: <http://www.ewh.ieee.org/r2/susquehanna>**April 2008 Section Meeting****Signal Integrity Symposium**

Date: Tuesday, April 8, 2008
Time: Registration: 8:30 AM
Symposium: 9:00 AM to 4:00 PM
Roundtable/Lunch: 12:15 to 2:00 PM
Place: Capital Union Building (CUB), Penn State Harrisburg, Middletown, PA 17057
Lunch Cost: \$16
Contact: To register and additional information (and direction), please contact Dr. Sedig Agili, 717-948-6109, ssa10@psu.edu, Dr. Aldo Morales, awm2@psu.edu, 717-948-6379, or Brenda Firestone at 717-948-6538, blf3@psu.edu.
Please let us know by April 1st, to have the proper head count

Mark your calendars for April 8, 2008. Building on last year's success, Penn State Harrisburg will host the Second Central PA Symposium on Signal Integrity, which will be a forum about High Speed Connectors/Interconnects with participation from distinguished speakers from Qualcomm, Intel, Missouri Science and Technology University (Formerly UMR), and Penn State University. This will be an opportunity to hear about the latest developments in signal integrity from international experts and to exchange ideas. Signal Integrity vendors will also be participating to show the latest test equipment. Please note that our IEEE Section is co-sponsoring this event. IEEE members are encouraged to attend the roundtable discussion, which will replace our April Susquehanna Section meeting.

Hear from the Experts

Dr. Tim Michalka, Qualcomm Incorporated

Topic: Signal Integrity in High-Performance Electrical Design

Dr. Michalka has spent the last 20 years working primarily on the electrical aspects of electronic packaging and interconnections. He has a bachelor's degree in electrical engineering from the University of Maine and a master's and Ph.D. in electrical engineering from Stanford University. He has worked for companies such as Digital Equipment Corporation, Carborundum Microelectronics, and Hewlett-Packard. He has developed power integrity analysis tools and managed a team delivering packaging design and signal

integrity solutions for PA RISC microprocessors and custom high-performance ASIC. He is currently with Qualcomm Incorporated, San Diego, CA where he has been a principal engineer and manager of the Power & Signal Integrity Group since 2005. His team works with engineers nationwide to deliver leading edge wireless communications integrated circuits and systems.

Dr. Mohsen Kavehrad, Penn State University

Topic: Toward 100G over Copper

Dr. Kavehrad is the W. L. Weiss (AMERITECH) Endowed Chair Professor of Electrical Engineering at The Pennsylvania State University. He holds a Ph.D. degree in Electrical Engineering from Polytechnic University. After working for companies such as Fairchild Industries, GTE, and Bell Laboratories he joined the Department of Electrical Engineering at the University of Ottawa as a full professor and director of the Broadband Communications Research Laboratory. He later was appointed founding director of the Center for Information and Communications Technology Research (CICTR). He also was the CTO and a vice president at Tele-Beam Inc., State College, PA, and has served as a consultant to major corporations and government agencies.

Dr. Yun Ling, Engineer/Manager, Digital Enterprise Group, Intel Corporation

Topic: Signal Integrity of Complex Systems

Dr. Ling's main responsibility at Intel is to define, develop, and enable high-speed physical interfaces for Intel platforms. His technical areas of expertise include time- and frequency-domain signal integrity modeling and testing, EMC compliance, and thermal/mechanical development. Prior to joining Intel, Dr. Ling worked for AMP, Inc. focusing on contact physics and mechanics research. He earned a Ph.D. in mechanical engineering from SUNY Binghamton. He also taught for six years at Southeast University, China.

Dr. James Drewniak, UMR/MST EMC Laboratory, Missouri University of Science and Technology

Topic: Via Transitions and Stubs at the PCB/Connector Interface: ISI and Jitter Consequences

James L. Drewniak holds a bachelor's, master's, and Ph.D. in electrical engineering from the University of Illinois at Urbana-Champaign. He joined the Electrical Engineering Department at Missouri S & T (formerly the University of Missouri-Rolla) in 1991 and is a faculty member in the Electromagnetic Compatibility Laboratory. His research and teaching interests include electromagnetic compatibility in

high-speed digital and mixed-signal designs, signal and power integrity, electronic packaging, electromagnetic compatibility in power electronic based systems, electronics, and antenna design. He has served as the chair of the EMC Society technical committees TC-10 Signal Integrity, and TC-9 Computational Electromagnetics, and is currently an associate editor for the IEEE Transactions on EMC.

Please note that our IEEE Section is sponsoring this event. For more information, contact Brenda Firestone at blf3@psu.edu, (717)-948-6538, or Dr. Aldo Morales, awm2@psu.edu or Dr. Sedig Agili ssa10@psu.edu. For more information please go to the Symposium's website at <http://php.scripts.psu.edu/dept/iit/cl/set/SignalIntegrity/>

March Section Meeting

53 people (30 IEEE members, 22 students from Elizabethtown College, and 1 speaker) attended the March meeting at Elizabethtown College. The evening consisted of a nice served dinner, good conversation, and a presentation on "Tidal and Wave Power – Projects, Recent Developments, and Possibilities" by Carl Atkinson III, P.E., Offer Project Manager, Voith Siemens Hydro Power Generation, York, PA. Mr. Atkinson discussed ocean wave and tidal power generation potential and technologies, as well as presenting information on Voith Siemens technology and ongoing projects and installation plans. This excellent talk touched on some of the renewed excitement on the possibilities and potential for the use of ocean energy, some of the continuing cost, and environmental and technical challenges currently being researched and addressed.

Useful Information Column

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Q/A-C Column

Do you have any questions or comments? Call, email or FAX it to our Chair at the above address.

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