

November/December IEEE Boise Section Update:

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Dear Boise IEEE Section Members,

I have a few things to update you about as my term in office comes to a close. I've really enjoyed working with all of you. I will still be an active section member, but just not quite as active. Fortunately we have some great people on next year's OPCOM, and the society chapters are also humming along with some great events planned. So look forward to some great events in the coming months. Please continue to support your local IEEE section and your new OPCOM board. I look forward to seeing you at an IEEE event soon.

1) New officers:

We had both very good luck and not so good luck finding new officer candidates this year. We had great luck in that we have several new faces joining our Operations Committee (OPCOM) this year who had not been there in past years. The poor luck was that we had only one nominee for each position and we have one position still unfilled (if you have a nominee in mind for it, please contact me or any other officer). According to our bylaws, if we don't have multiple nominees for offices, we don't need to hold an election, so this year we decided not to hold an election. If you were waiting for your ballot, that is why it never came. Our list of new officers is:

Chair: Brooklin Gore (Micron)  
Vice Chair: \*\*\*\*\* vacant \*\*\*\*\*  
Treasurer: Erwin Schweibert (Schweibert Group)  
Secretary: Shawn Hilde (Quality Thermistor Inc)  
Membership Chair: Liz Weese  
PACE Chair: Terry Burkes (HP)  
Speakers Chair: Luan Tran (Micron)  
Webpage Editor: Drew Dunn (Micron)  
Newsletter: Scott Jeide (Extended Systems)  
GOLD Chair; Randy Wolff (Micron)

When you see them, please welcome them and thank them for allocating some of their valuable time to the section.

2) Next OPCOM Meeting

The section's next OPCOM meeting will be at 6pm on Wednesday 3 December. It will be held at Idaho Pizza on Broadway. All section members are always invited to attend the OPCOM meetings. The section will buy pizza or salad bar for any section member in attendance. We hope to see many of you there. Come and welcome our new OPCOM as they get ready for the new year.

3) Help wanted (Job openings)

If anyone knows of any job openings in your company that can help out members of our section, please let us know. We'll put a short notice in our email updates, and a full listing on our section website. BSU will start the process by advertising for some adjunct faculty.

The Boise State University Department of Electrical and Computer Engineering is seeking adjunct faculty to teach several laboratory classes and lecture classes. The laboratory classes are second semester linear circuits, introductory digital systems, and introductory microprocessors. The lecture classes are control systems and second semester linear circuits. Digital systems instructors should be familiar with digital switching theory, 7400 series logic devices, and simple programmable logic devices. Microprocessors instructors should be familiar with x86 assembly language programming and ISA bus interfacing. Those interested in any of these positions should contact: Dr. John Gardner, Department Chair, Electrical and Computer Engineering, (208) 426-5702 or JGardner@BoiseState.edu.

#### 4) Upcoming Events

We have two major events coming soon: National Engineers week and an EDS Tutorial on "The Future of Semiconductor Manufacturing". We hope to see lots of members at each of these events.

\*\* National Engineers Week is scheduled for 22-28 February 2004. Nationally the IEEE is the lead professional society for this year's event (the societies take turns). We are planning our annual banquet to coincide with this week. We are working on getting a great speaker. Please mark this in your calendars now and plan to attend. Support the section, have a great meal, and socialize with your peers. We hope to see you there!

\*\* IEEE Electron Devices Society, Boise Chapter Tutorial on "The Future of Semiconductor Manufacturing"

A Videotaped Tutorial from the 2002 IEDM  
Organized by Jack Y.C. Sun, TSMC

Lecturers: Bruce Sohn, Intel (Manager of Fab 11X); Rinn Cleavlin, TI; N. Kobayashi, SELETE; Henry Chen, TSMC; and P.K. Mozumder, PDF Solutions

WHEN: Two classes will be offered, each with two sessions: November 20 (1:00-4:00pm) & November 21 (1:00-5:00pm) December 15 (1:30-5:30pm) & December 16 (1:00-4:00pm)

WHERE: Micron Technology, Inc.  
8000 S. Federal Way  
Boise, ID 83707  
Bldg 17C, ROOM 117A/B

#### REGISTRATION:

CONTACT: Gail Hawkins (ghawkins), Micron Corporate Training & Education, 368-4331  
Joseph Wang (jwang), Micron R&D, 368-2076

See the IEEE Boise website (<http://ewh.ieee.org/r6/boise> ) for more details on all section events.