

Senior CMOS Design Engineer Position

Responsibility: Design high-speed electronics for lightwave transmitter/receivers

Job description: Team leader and principal contributor for CMOS circuit design and implementation, including TIA, Limiting amplifiers, logic circuits, CDR, PLL frequency synthesizers, TDM, channel equalizers, multiplexers, synchronization circuitry. At least 2 years of hands on experience with CADENCE design kits. Candidate will be responsible for all phases of design engineering from circuit simulation to layout to overseeing prototype fabrication. Design experience with chip speeds of 10 Gb/sec and higher will be required. M.S. or Ph.D. in electrical engineering or computer science, with > 2 years of in-depth industry experience and understanding of 130 nm (required) and 90 nm (highly desired) CMOS design rules and digital circuitry are required. Must be strong in problem solving and organizing/leading team consisting of engineering staff, external contractors, vendors, and fabrication facilities. Must have completed 3 or more chips from floor plan to successful demonstration. Must have contributed methodology solutions and scripting to design flows used by others.

Photonic Corporation is applying proprietary design and process technology to develop highly integrated photonic and electronic components for optical interconnections and optical sensors. Our approach to opto-electronic components is based on CMOS compatible processes and uses established semiconductor infrastructure. Photonic has a state-of-the-art wafer fab and years of experience in delivering for defense contracts.

Contact information:

Koichi Sayano

sayano@photonic-corp.com

Tel: 310 417-3652 x110

<http://www.photonic-corp.com>