

R&D Engineer, Sr II (Debugger Developer)

Req. 818BR

Location: SV

Recruiter: Jeff Vise 512-372-7555

Job Description and Requirements We are a team working on producing the high quality development toolchain for ARC family of 32-bit configurable processors. We are looking for an engineer like you to be part of the team to work on a world-class feature-rich debugger that delivers a highly-detailed look at how embedded software functions on ARC IP.

Responsibilities:

Develop and implement new enhancements and features in the debugger, GUI and the instruction set simulator

Maintain the current debugger and simulator products

Interact with Hardware and Application developers to improve the products for a rich detailed embedded debugging experience

Perform various benchmarking and engineering testing tasks to improve the quality of the debugger products

Assist product marketing and product support teams with pre and post sales situations as needed

Qualifications:

Minimum of 8+ years of related experience

Thorough understanding of the debuggers, instruction set simulators and other related development tools internals

Extensive knowledge and experience with the debugger development

Expert-level programming skills in C++, C, Java and assembly languages

Expert-level design, coding and debugging skills

Knowledge of ARC processors and ISA is a plus

Experience with the implementation of embedded processor development tools is desirable

Experience with the development tools such as SVN and Bugzilla

Excellent teamwork and communication skills

BSCS degree or equivalent, MSCS preferred