

Job Description

Contact Person: Jeff Chin, jeffchin@tsmc.com

Duration of Job Posting: Oct. 29, 2009 to Nov. 12, 2009

1. Flash Designer - (LOCATION: TSMC North America - San Jose)

Job Responsibilities include, but are not limited to:

- Design flash memory IP's
- Block-level and full-chip level simulations. Ensure circuit robustness and quality deliver.
- Propose innovative ideas to improve circuit performances.
- Document design materials (bitmaps, internal user/test mode document, design review materials, and meeting minutes).
- Publish papers or patents regularly.
- Supervise layout work.

Qualifications:

- M.S.E.E. or related field plus 5+ year experiences in circuit design, flash memory design experience preferred.
- Good team member and communication skills.
- Experience in read path, high voltage path or analog design is a plus.
- Creativity and innovation capability (papers, patents of any field) is a plus.
- Familiar with tape-out procedure.

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