

IEEE Signal Processing Society, Santa Clara Valley Chapter

Workshop on FPGAs for Digital Signal Processing Applications

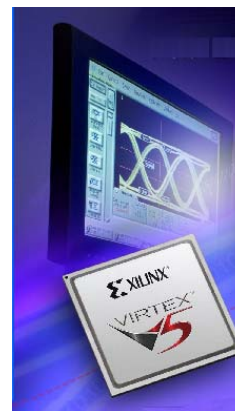
Saturday, Feb. 7, 2009, 8:00am – 5:00pm
Bannan Engineering Laboratories, Santa Clara University

Workshop Website and Registration

<http://ewh.ieee.org/r6/scv/sps/fpgaSPS/>

Topics at a glance

- (1) Why and when to use DSPs, FPGAs and ASICs
Speaker: Dr. Xiaoshu Qian, Intel Corporation
- (2) FPGA Architectures for H.264 Video Processing
Speaker: Prof. Tokunbo Ogunfunmi, Santa Clara University
- (3) FPGAs: Re-inventing the Signal Processor and MIMO-OFDM Applications
Speaker: Dr. Chris Dick, Xilinx Corporation
- (4) FPGA-Based Embedded Wideband Audio Codec System
Speaker: Prof. Chang Choo, San Jose State University
- (5) A Platform-Based Approach to Realizing High-Performance DSP Subsystems in FPGAs
Speaker: Dr. Jim Hwang, Xilinx Corporation



Santa Clara University

Tentative Agenda

8:00 am - 8:30 am	Registration/breakfast
8:30 am - 8:45 am	Welcome address/introduction
8:45 am - 9:15 am	Topic 1: Why and when to use DSPs, FPGAs and ASICs
9:15 am - 9:20 am	Break
9:20 am - 10:50 am	Topic 2: FPGA Architectures for H.264 Video Processing
10:50 am - 11:00 am	Break
11:00 am - 12:30 pm	Topic 3: Re-inventing the Signal Processor and MIMO-OFDM Applications
12:30 pm - 1:30 pm	Lunch
1:30 pm - 3:00 pm	Topic 4: FPGA-Based Embedded Wideband Audio Codec System
3:00 pm - 3:10 pm	Break
3:10 pm - 4:40 pm	Topic 5: A Platform-Based Approach to Realizing High-Performance DSP Subsystems in FPGAs
4:40 pm - 5:00 pm	Q&A, wrap-up panel discussion: Engineering Challenges and Business Opportunities

Registration Fees

	Before Jan 27	After Jan 27 or on-site
IEEE member	\$70	\$80
Non-IEEE member	\$80	\$90
Student	\$30	\$40

Registration includes a copy of the workshop proceedings, lunch, breakfast, and coffee.

Contacting the Workshop

- SPS SCV Chair: **Tokunbo Ogunfunmi**, email: tokunbo@ieee.org
- SPS SCV Program Coordinator: **Yen-Kuang Chen**, email: y.k.chen@ieee.org
- SPS SCV Chapter Website: <http://ewh.ieee.org/r6/scv/sps/>