

Advanced Program



1st International Workshop on
**Compact Thin-Film
Transistor Modeling for Circuit Simulation**
September 11, 2008
The Møller Centre, Cambridge, UK



8:30 AM: **Registration and Breakfast**

9:30 AM: Introduction, Samar Saha, *Chair, IEEE EDS Compact Modeling Technical Committee, Silterra USA, Inc.*

9:40 AM: Welcome Address, William Milne, *Cambridge University, Cambridge, UK.*

10:00 AM – 12:15 PM: Session Chair: William Milne, Cambridge University, Cambridge, UK.

10:00 AM: Compact TFT device models for circuit simulation (invited), M.S. Shur, *Rensselaer Polytechnic Institute, Troy, New York, USA.*

10:45 AM: Universal compact modeling of thin-film transistors for circuit simulation (invited), B. Iñiguez, *DEEEA, Universitat Rovira i Virgili, Avinguda dels Països Catalans, Spain.*

11:30 AM: Non-ohmic contact resistance and field effect mobility in silicon thin-film transistors (invited), A. Nathan, A. Ahnood, and K. Ghaffarzadeh, *London Center for Nanotechnology, University College London, London, UK.*

12:15 PM – 1:45 PM: **Lunch Break**

1:45 PM – 5:00 PM: Session Chair: Arokia Nathan, London Center for Nanotechnology, University College London, London, UK.

1:45 PM: Modeling organic/polymeric thin-film transistors (invited), M.J. Deen, *Department of Electrical and Computer Engineering, McMaster University, Hamilton, ON, Canada.*

2:30 PM: Charge carrier injection and transport through the contacts of organic field effect transistors (invited), J.A. Jiménez Tejada*, and P. Lara Bullejos, *Departamento de Electrónica y Tecnología de Computadores, Facultad de Ciencias, Universidad de Granada, Avenida Fuentenueva s/n, Granada, Spain;* M.J. Deen, *Department of Electrical and Computer Engineering, McMaster University, Hamilton, ON, Canada.*

3:15 PM: Modeling of organic thin-film transistors for technology and circuit design (invited), A. Rankov, E. Smith, J. Halls, T. Kugler, and C. Newsome, *Cambridge Display Technology Limited, Cardinal Park, Godmanchester, UK;* S. Mijalković, D. Green, and A. Nejm, *Silvaco Data Systems (Europe) Ltd, Silvaco Technology Centre, St Ives, Cambridgeshire, UK.*

4:00 PM: Modelling of organic semiconductor diodes and transistors for circuits (invited), W. Eccleston, *Organic Electronics Group, University of Liverpool, Liverpool, UK.*

4:45 PM: EDS Mini-colloquia announcement, Ali Rezazadeh, *Chair, IEEE UK-RI (AP/ED/LEO/MTT) Joint Chapter, Manchester University, Manchester, UK.*

4:50 PM: Closing Remarks: M. Jamal Deen, *Department of Electrical and Computer Engineering, McMaster University, Hamilton, ON, Canada.*

Registration fee:

IEEE Members: £80.00

Students: £40.00

Non-IEEE members: £100.00

A number of accommodations has been reserved at the Møller centre for Workshop attendees. Please contact: **Marius Bauza, Email: m.bauza@ucl.ac.uk**