



**EDS Mini-Colloquium
On**

Advanced Electron Devices Technology & Modeling

September 12, 2008

The Møller Centre, Cambridge, UK

Sponsored by

IEEE Electron Devices Society

8:30 AM: Sign up

9:00 AM: Opening Remarks, Prof. A. Reza zadeh, *Chair, IEEE UK-RI (AP / ED / LEO / MTT) Joint Chapter, Manchester University, Manchester, UK.*

9:10 PM: IEEE EDS Activities: Dr. S.K. Saha, *Chair, IEEE EDS Compact Modeling Technical Committee & SRC-NAW, Silterra USA Inc., USA.*

9:30 AM – 12:30 PM: Session I

Chair: Prof. A. Nathan, *London Center for Nanotechnology, University College of London, London, UK.*

9:30 AM: Compact modeling of silicon-based, low-cost, highly integrated biosensors, Prof. M.J. Deen, *Department of Electrical and Computer Engineering, McMaster University, Hamilton, ON, Canada.*

10:30 AM: Design and modeling of multilayer technology for 3D MMIC applications, Prof. A. Reza zadeh, *Manchester University, Manchester, UK.*

11:30 AM: Small-signal compact modeling of multiple-gate MOSFETs, Prof. B. Iñiguez, *DEEEA, Universitat Rovira i Virgili, Avinguda dels Països Catalans, Spain.*

12:30 PM – 2:00 PM: **Lunch Break**

2:00 PM – 5:00 PM: Session II

Chair: Prof. A. Reza zadeh, Prof. A. Reza zadeh, *Manchester University, Manchester, UK.*

2:00 PM: Compact advanced SOI devices: Typical mechanisms and characterization, Prof. S. Cristoloveanu, *Institute of Microelectronics, Electromagnetism and Photonics, Grenoble Cedex 1, France.*

3:00 PM: Nanoscale large area electronics – device and material integration challenges, Prof. A. Nathan, *London Center for Nanotechnology, University College of London, London, UK.*

4:00 PM: Formation of composite organic thin film transistors with nanotubes and nanowires (student), Chien-Wen Hsieh, *Electrical Engineering Division, Cambridge University, Cambridge, UK.*

4:30 PM: *, TBD (student talk).

5:00 PM: Closing Remarks: W.I. Milne, *Electrical Engineering Division, Cambridge University, Cambridge, UK.*