

IEEE Region 6 Awards – 2011

NOMINATION FORM

(for Area Awards, and also for Region 6 Awards)

(This Nomination form is to be used by Sections for Area Award Nominations for all categories. This form is also to be used by Sections-via-Areas for Region 6 Award Nominations. Please use one form per Nomination.)

Area → Central, NE, NW, Southern, SW

Award Category of the Nomination:

- Outstanding Engineer Award
- Outstanding Leadership and Professional Service Award
- Outstanding Section Award
- Outstanding Corporate Service to Engineering Community Award
- Outstanding Student Branch Award
- Director's Special Award / Area Chair's Special Award

Name (Individual/Section/Student-Branch/Company): Randall G. Redondo

Address: 1988 Fielding Hill Lane, Draper, UT 84020

Phone: 801 360 2896 e-Mail: rredondo@ieee.org

Occupation: Engineering Manager

Title/Position: Engineering Manager, RSM business unit, Grass Valley Inc.

IEEE Grade: Senior Member Date of Grade: 18 June 2005

Company: Type of Business, # of employees, etc. Professional Broadcast OEM,
Worldwide operations with 1600 employees

Proposed Citation: For outstanding Leadership and Professional Service to the IEEE

Supporting information to be attached, as appropriate and applicable:

- (a) Engineering Career (Brief Summary), (b) Education, (c) Research/inventions, (d) Contributions to engineering literature, (e) Contributions to IEEE activities, (f) Contributions to the community
- Outstanding accomplishments of the Section
- (a) Company contributions to engineering, (b) Company support of IEEE activities, (c) Company contributions to the community (monetary and other relevant contributions)
- Outstanding accomplishments of the Student Branch
- Region 6 Director's Special Award / Area Chair's Special Award

Utah Section

Section Name



Signature of Nominator (Section Awards Chair or Section Chair)

801-643-6644/jennifer.hershman@hill.af.mil

Phone / e-Mail

Jennifer Hershman

Name of Nominator (Section Awards Chair or Section Chair)

Awards Chair

Title of Signer/Nominator

April 26, 2011

Date

Signature of Area Awards Chair or Area Chair (for Region Awards)

Date



TO: ELISA BARNEY SMITH, IEEE REGION 6 NORTHEAST AREA CHAIR
FROM: JENNIFER HERSHMAN, IEEE UTAH SECTION AWARDS CHAIR
SUBJECT: NOMINATION – RANDALL G. REDONDO - OUTSTANDING LEADERSHIP AND PROFESSIONAL SERVICE AWARD
DATE: APRIL 26, 2011

IEEE UTAH SECTION takes great pleasure in nominating Mr. Randall G. Redondo for the Outstanding Leadership and Professional Service award. Following is a brief summary of Mr. Redondo's qualifications for the award. Please see Mr. Redondo's attached resume for further details.

ENGINEERING CAREER

Mr. Redondo has been working for sixteen years in research and development in the field of Professional Audio and Video Broadcast. Equipment developed by Mr. Redondo has been used in all major television networks including, Disney, ESPN, Turner Broadcast, ABC Network, CBS, CBC, PBS, and other worldwide Television Networks. Most recently Mr. Redondo has been promoted to the position of Engineering Manager. In this role Mr. Redondo is responsible for an \$8 Million R&D budget.

EDUCATION

Mr Redondo holds a bachelors degree in Electrical Engineering from the University of Utah.

CONTRIBUTIONS TO IEEE ACTIVITIES

Mr. Redondo is a Senior Member of the IEEE. Mr. Redondo has served in numerous IEEE positions including: Region 6 Membership Development Chair (current), Region 6 OpCom (current), Utah Section ExCom (current), Utah Section Signal Processing/Communications Chapter Chair (current), Utah Section Chair, Utah Section Vice-Chair, Utah Section Technical Programs Chair, Utah Section Senior Member Committee.

Mr. Redondo has been an active member of the Utah Section Executive Committee for six years. Most recently serving as the Section Chair for two terms from 2008 to 2009. During this time Mr. Redondo worked to;

- Re-energize the Section activities and membership participation.
- Increase the Section's active ExCom volunteers from six members in early 2008 to twelve members in late 2009.
- Increase the number of Section technical and social activities and membership attendance.
- Restart the Section's GOLD activities leading to the nomination of multiple volunteers to run the GOLD Affinity Group.
- Form a PES chapter in early 2009 and the groundwork was started for the formation of an EMC chapter and a WIE Affinity group.
- Improved membership communications by creating a LinkedIn group for Section members and a LinkedIn group for the Section ExCom.
- Successfully identify and nominate at least two candidates for each of the Section's elected officer positions. This represented a departure from many previous Section officer elections.

Mr. Redondo worked to increase the Section's visibility within the IEEE by successfully hosting the 2008 AUTOTESTCON conference and the 2009 IEEE USA meeting. These successful events directly contributed to the Section being selected to host the Region 6 fall meeting in September 2010.

Mr. Redondo has been an active member of the Region 6 OpCom for the past four years. Most recently he has served as the Region 6 Membership Development Chair. During this time Mr. Redondo worked to;

- Revitalize the Region member development program to increase membership value.
- Develop effective Region Officer Membership Development programs resulting in measureable results.
- Achievements:
 - Region 6 won the 1st Place in the IEEE MGA Higher-Grade Retention Challenge for 2010
 - Region 6 won 3rd Place in the IEEE MGA Total Membership Retention Challenge

IEEE RECOMMENDATIONS

Mr. Redondo has received numerous recommendations from many coworkers and Utah Section volunteers. These recommendations are posted in Mr. Redondo's LinkedIn profile at <http://www.linkedin.com/in/rgredondo>.

RANDALL G. REDONDO, BSEE

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EMPLOYMENT HISTORY HIGHLIGHTS

□ **Grass Valley, Salt Lake City, Utah**

Dates of Employment: 2000 - Present

Title: *Engineering Manager (3/2010 – Present)*

Responsibilities: Confer with management, production, and marketing staff to discuss project specifications and procedures. Coordinate and direct projects, making detailed plans to accomplish goals and directing the integration of technical activities and resource allocations. Analyze technology, resource needs, and market demand, to plan and assess the feasibility of projects. Plan and direct the installation, testing, operation, maintenance, and repair of development laboratory facilities and equipment. Direct, review, and approve product design and changes. Prepare present and review the annual engineering development budget. Recruit hardware and software design engineers and supporting staff; assign, direct, and evaluate their work; and oversee the development and maintenance of staff competence. Develop and implement policies, standards and procedures for the engineering and technical work performed in the engineering department. Set scientific and technical goals within broad outlines provided by top management. Perform administrative functions such as reviewing and writing reports, approving expenditures, enforcing rules, and making decisions about the purchase of materials or services. Present and explain proposals, reports, and findings to upper management and customers. Consult or negotiate with clients to prepare project specifications.

Highlights of Achievements:

- **Technical Leadership:** Identify and mentor technical leadership to lead development teams to deliver high quality products meeting their project schedule and financial targets. This effort has resulted in measureable improvements in the quality and schedule of all development projects.
- **Skill Development and Training:** Develop and implement effective training programs to improve the technical skills of the engineering development team. Engaged development team members to identify, research, and present engineering technical seminars to improve the knowledge and expertise transfer across the organization.

Title: *Senior Electronic Design Engineer (2003 – 3/2010)*

Responsibilities: Technical Lead of multi-disciplinary and multi-geographic design teams in product concept, design, development, and manufacturing. Implement and maintain FPGA based design methodologies to ensure optimal and efficient resource utilization. Interface with customers to ensure the successful implementation and integration of new products and enhancements.

Highlights of Achievements:

- **FPGA based system development:** Implement industry leading design practices and development tools to achieve high performing FPGA based systems. Developed multiple FPGA based MACs to support a self healing and hot swap interface used on multiple products that serve mission critical on-air applications.

- **IP Development:** Key contributor in the development of a new digital audio router leading to several patent applications pending US and EU approval. Development of numerous hardware and gateware design trade secrets used in several market leading products.
- **Career development and mentoring:** Participate in the interview process of new hardware engineer candidates making hiring recommendation directly to the hiring manager. Selected to mentor new hardware engineers to ensure their effective integration into our design teams. Development of training materials and courses to maintain and enhance the engineering team skills set.

Title: *Electronic Design Engineer (2000 – 2003)*

Responsibilities: Development of products from commercial requirements specifications with responsibilities for component qualification and selection, safety and environmental issues, quality control and technical training for service personnel. Design, simulate, implement, and verify FPGA based solutions to product technical requirement specifications.

Highlights of Achievements:

- **EDA and FPGA Tools:** Implemented command line scripts to ease the development of FPGA based designs. Installed, configured, and maintained the engineering floating license server for simulation, synthesis, and implementation software tools. Lead effort to implement the FPGA development tools on Linux platforms resulting in significant improvements to tool run times.
- **HDL:** Implemented coding for FPGA designs using Verilog HDL running up to 156 MHz adhering to coding styles that minimized logic levels to meet timing requirements.
- **Electromagnetic and safety compliance:** Implemented working design rules for product enclosures reducing the post development design changes needed for compliance to testing standards.

□ **Philips Digital Video Systems, Salt Lake City, Utah**

Dates of Employment: 1995 - 2000

Title: *Electronic Design Engineer (1997-2000)*

Responsibilities: Developed solutions for product component obsolescence in production products. Responsible for new and existing product electromagnetic, safety, and environmental compliance issues.

Highlights of Achievements:

- **FPGA Development:** Project lead responsible for the implementation of new product features into existing released products. Responsible for design conversion and testing of multiple FPGA based designs to replace obsolete components.
- **Electromagnetic and safety compliance:** Increased the efficiency of certification testing by working closely with testing laboratories in the development of testing methods that comply with targeted standards.
- **Power Distribution:** Introduced designs utilizing self-cooling front-end hot-swappable power supplies. The adaptation of these designs into new products has reduced product cost and shortened time to market.

EDUCATION

- ❑ **University Of Utah, Salt Lake City, Utah**
Degree: **BSEE** with emphasis in **communications and digital signal processing**.
- ❑ **Westchester Community College, Valhalla, New York**
Degree: **A.A.S.** in Electrical Technology **with honors**

PATENTS

- ❑ “System and Method for Routing Asynchronous Signals”. US patent application number: 20080052552, International patent application number: PCT/US2005/019115.
- ❑ “Fault Tolerant ISP of FPGA-based modules via JTAG”. International Application number: PCT/US2006/044951.

DEVELOPMENT TOOLS

Xilinx’s ISE FPGA design suite, Altera’s Quartus FPGA design suite, Mentor Graphics’ ModelSim, Synplcity’s Synplify PRO, Cadence CAE tools, Cygwin, XEmacs, FlexLM license server, Windows, and Linux.

PROFESIONAL ACCOMPLISHMENTS & AWARDS

- ❑ Senior member of the Institute of Electrical and Electronics Engineers (IEEE) 2005.
- ❑ IEEE Utah Section Outstanding Leadership and Professional Service Award 2010.
- ❑ IEEE Utah Section Engineer of the Year Award 2009.
- ❑ Utah Engineers Council Engineer of the Year Nominee, 2009
- ❑ IEEE Utah Section Outstanding Engineer Award, 2008.

ACADEMIC ACCOMPLISHMENTS & AWARDS

- ❑ National Scholar at Large Award from the National Achievement Academy, 1993.
- ❑ National Academic Achievement Award in Electrical Engineering Technology from the National Achievement Academy, 1993.
- ❑ President of the Institute of Electrical and Electronics Engineers student branch, 1992-93.
- ❑ President of the school chapter of Tau Alpha Pi, 1992-93.
- ❑ Member of the Student Senate, 1990-93.
- ❑ Member of the Orientation Committee, 1990-93.

PROFESSIONAL ACTIVITES

- ❑ Membership Development Chair of the IEEE Region 6 (2010-11)
- ❑ Chair of the IEEE Utah Section (2008-09).
- ❑ Vice Chair of the IEEE Utah Section (2006-07).
- ❑ Programs Chair of the IEEE Utah Section (2005).

TECHNICAL PRESENTATIONS

- ❑ “Application of FPGA technologies in modern broadcast equipment designs”, Brigham Young University, Electrical and Computer Engineering Department, 2005.

- ❑ “An FPGA based 3.125 Gbps Serial Interface for Professional Audio for Television and Radio Broadcast”, University of Utah, Electrical and Computer Engineering Department, 2003.

LANGUAGES

- ❑ Technical: VHDL, Verilog HDL, Perl, Tcl/Tk, C++ and C.
- ❑ Foreign: Fluent Spanish.

VOLUNTEER WORK

- ❑ Neighborhood Representative for the Suncrest HOA (2008).
- ❑ American Preparatory Academy (2003-04): Designed, implemented, and configured the LAN infrastructure to enable the school’s administration and teachers to share networked equipment and files.
- ❑ Hidden Spring Condominiums HOA President (1999-2005).
- ❑ Utahns Against Hunger (1997): Implemented and configured the LAN infrastructure to enable charity staff and volunteers to share files and networked resources.
- ❑ American Red Cross (1990-95): Certified instructor of first aid and CPR. Selected volunteer of the emergency response team providing transport to medical personnel during severe winter storms.