

# Seventh IEEE Dallas Circuits and Systems Workshop

October 19 - 20 2008 The University of Texas at Dallas



# System-on-Chip (SoC): Design, Application, Integration and Software

http://ewh.ieee.org/soc/cas/dallas/wks2008/

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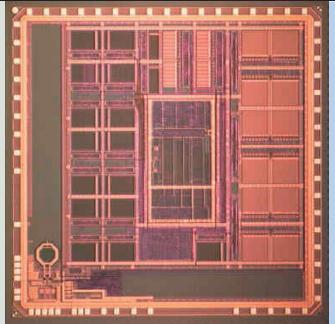
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#### Technical Program

The technical program committee invites people from academia and industry to contribute new and previously unpublished results in the following SoC related areas:

High Performance & Low Power Circuits Digital Signal Processors and Cores Baseband Communication Processors RF Processors and Circuits Multimedia Processors Reconfigurable Processors High Speed Digital Circuits Low Voltage and Mixed Signal Circuits Mixed Signal Integration Clocking and Clock Distribution A/D and D/A Conversion High Speed I/O Power Management for SoC Signal Integrity and On-Chip Interconnects SoC Implementation Methodology SoC Verification Methodology SoC and IP Integration

Systems-on-Chip (SoC) with giga-scale integration of IP are the main drivers of the semiconductor industry today. From the handsets and multimedia consumer electronics devices to communication infrastructure, the SoC's create new challenges in design techniques, circuits, integration, power management, packaging and test. The IEEE Dallas Section, Circuits and Systems Society is conducting a two day workshop (DCAS-08) to provide a forum for sharing Design, Application, Integration and Software of System on Chip.

DCAS-2008 is sponsored by the IEEE Circuits and Systems Society (Dallas Chapter), and will be held on the campus of The University of Texas at Dallas.

Papers will be accepted for lecture or poster presentation. All accepted papers will be printed in the conference proceedings and online at IEEE Xplore.

#### Submission Guidelines

Authors are invited to submit a paper draft, not to exceed four pages. Submissions should be made directly using upload facility that is accessible from workshop website. The Submission must include the title, authors names, affiliations, and contact e-mail. Please contact Dr. Bogdan Staszewski (b-staszewski@ti.com) for submission related questions.

#### **Paper Submission Details**

Submission Deadline: Sep. 5, 2008 Acceptance Notice: Sept. 22, 2008 Camera Draft Due: Oct. 3, 2008

#### **Submission Website:**

https://cmt.research.microsoft.com/DCAS2008/