2008 IEEE Dallas Circuits And Systems Workshop Program

DAY I: Sunday, Oct 19, 2008

10:15 AM   POSTER SETUP

10:55 AM   OPENING REMARKS

11:00 AM   An Accurate Gate-Delay Model for High Speed Digital and Analog Circuits
Josef Dobeš[1], Václav Panko[12], and Ladislav Pospíšil[1]
[1]Czech Technical University in Prague, [2]ON Semiconductor

11:20 AM   Active Inductor for Power-Supply Decoupling in Mixed Signal Systems
Ajay Taparia[1], TR Viswanathan[2], Bhaskar Banerjee[1],
[1]University of Texas at Dallas, [2]University of Texas at Austin

11:40 AM   Two Zero and Two Pole Active Compensation Replaces a Charge Pump in PLLs
Stanley J. Goldman, Texas Instruments

12:00 PM   LUNCH

**1:00 PM   Invited Talk 1: Enabling Breakthroughs in Medical Electronics
Mr. Kent Novak, Texas Instruments, Dallas TX**

**2:15 PM   Invited Talk 2: Towards 4G Wireless Systems
Dr. Jayant Kulkarni, Award Solutions Inc., Dallas, TX**

3:30 PM   BREAK

3:30 PM   SUNDAY POSTERS

Real-Time Optimization of Viola -Jones Face Detection for Mobile Platforms
Jianfeng Ren[1], Nasser Kehtarnavaz[1], and Leonardo Estevez[2]
[1]University of Texas at Dallas; [2]Texas Instruments

Accuracy and Repeatability of Noise Measurements with a Discrete
Fourier Transform
Ross A. Kulak, Texas Instruments

A 6-bit 300-MS/s 2.7mW ADC Based on Linear Voltage Controlled Delay Line
Chaoming Zhang, Jacob A. Abraham and Arjang Hassibi, University of Texas at Austin

Automatic Generation of Random Stimuli Sources based on Parameter
Domains for Functional Verification
Carlos I. Castro, Edgar L. Romero, Marius Strum, and Wang J. Chau,
University of Sao Paulo, Brazil

Developing a Fast and Inexpensive Low Power Design Strategy
Mandeep Singh, Christophe Giacomotto, Vojin G. Oklobdzija
University of California at Davis, University of Texas at Dallas

A Linearized CMOS Quad using selective even feedback
Venkatesh Acharya[1], Bhaskar Banerjee[1] and T.R.Viswanathan[2]
[1]University of Texas at Dallas, [2]The University of Texas at Austin

4:15 PM   POSTER REMOVAL

2008 IEEE Dallas Circuits And Systems Workshop
Program (continued)

DAY II: Monday, Oct 20, 2008

7:30 AM BREAKFAST & POSTER SETUP

8:10 AM OPENING REMARKS

8:30 AM **Keynote Address:**
Survival of VLSI Design - Coping with Device Variability and Uncertainty
Dr. Kevin Nowka, IBM Research Labs, Austin, TX

9:45 AM BREAK

9:55 AM **Invited Talk 3: Advanced Digital Linearization Approaches for Wireless RF Power Amplifiers**
Prof. Larry Larson, University of California at San Diego, CA

11:10 AM A highly integrated GPS front-end for cellular applications in 90nm CMOS
Naveen K. Yanduru and Kah-Mun Low, Texas Instruments

11:30 AM Interference Cancellation in Receivers with Interference Frequency Estimation
Chih-Hao Sun, Sahar Ayazian, Xin Wang, and Ranjit Gharpurey,
University of Texas at Austin

11:50 AM LUNCH

1:30 PM **Invited Talk 4: Progress Toward a Single Chip Radio in CMOS**
Prof. Ken O, University of Florida, FL

2:45 PM A CMOS Differential Noise Cancelling Low Noise Transconductance Amplifier
Xi Chen, Jose Silva-Martinez, and Sebastian Hoyos, Texas A&M University

3:05 PM Limited Bandwidth Envelope Follower for Improving Efficiency of
Broadband Linear Power Amplifier
Sankalp Modi[1], Sunilduth Kanigere[1], Oren Eliezer[2], Poras Balsara[1]
[1]University of Texas at Dallas, [2]Texas Instruments

3:25 PM BREAK

3:30 PM MONDAY POSTERS

A Low-IF WiMAX RF Transceiver in 0.18um CMOS.
Ajay Taparia, Syed Askari Nakvi, and Bhaskar Banerjee, University of Texas at Dallas

Bottom up Approach to Enhance Top Level SoC Verification
Guha Lakshmanan, Sudhind Dhamankar, Sandeep Tare, Vipin Sharma,
Texas Instruments

On the Portability and Performance of Fully Monolithic Transformer Structures for
RF Power Amplifiers in Standard CMOS Process
Jerry Lopez[1], Donald Y.C. Lie[1], R.Bogdan Staszewski[2], Daquan Huang[2], Chih-Ming
Hung[2], and Sankaran Swaminathan[2]
[1]Texas Tech University, [2]Texas Instruments

The Dynamic Stability of a 10T SRAM compared to 6T SRAMs
at 32nm Node Using an Accelerated Monte Carlo Technique
Anand Seshadri and Theodore W. Houston, Texas Instruments

Envelope and Phase Paths Recombination in ADPLL-based Wideband Polar
Transmitters
Ioannis L. Syllaios[1], Poras T. Balsara[1], Robert Bogdan Staszewski[2]
[1]University of Texas at Dallas, [2]Texas Instruments

4:30 PM POSTER REMOVAL