

# 2004 Non-Volatile Semiconductor Memory Workshop

20th IEEE NVSMW

August 22 - 26, 2004

Hyatt Regency

Monterey, CA

## CALL FOR PAPERS

The 20<sup>th</sup> IEEE Nonvolatile Semiconductor Memory Workshop (NVSMW) will be held at the Hyatt Regency Hotel in Monterey, California from August 22 to 26, 2004. The workshop is sponsored by the IEEE Electron Devices Society.

The NVSMW is a unique forum for specialists in all aspects of nonvolatile memory microelectronics, and people with different backgrounds who wish to gain a better understanding of the field. The morning and afternoon technical sessions are organized in a manner to provide ample time for informal exchanges amongst presenters and attendees. The evening panel discussions will address hot topics in the nonvolatile field.

Papers are solicited in the aspects of nonvolatile semiconductor memory technology, including:

- Device Physics
- Cell Design
- Integrated Circuits
- Processing
- Testing
- Quality and Reliability
- New NVM Concepts and Technologies
- Solid State Disks, Memory Cards
- Programmable Logic
- New Applications

## SUBMITTING A PAPER

Please submit a one-page abstract with one page of supporting figures to the Technical Chairman at the address below. Electronic submission of the abstract using Adobe Acrobat (preferred) or Microsoft Word is strongly encouraged. The deadline for submitting abstracts is **April 26, 2004**. A book of proceedings consisting of a bound copy of abstracts for all accepted papers will be handed out to attendees at the conference. Also, a CD-ROM with the electronic copies of accepted abstracts will be provided following the workshop. In keeping with the workshop nature of the meeting, no photographs or recordings will be allowed.

Submissions from universities are encouraged. There is a subsidized registration fee for students and IEEE Members. The maximum attendance at the workshop is limited. Therefore, advance registration is highly recommended.

## CONTACT INFORMATION

### General Chairman:

Kelly Baker  
M/S OE341, Motorola, Inc.  
6501 William Cannon Dr. West  
Austin, TX 78735, USA  
Phone: (512) 895-8335  
FAX: (512) 895-8605  
E-mail:

[kelly.baker@motorola.com](mailto:kelly.baker@motorola.com)

### Technical Chairman:

Andrei Mihnea  
M/S 1-716, Micron Technology, Inc.  
8000 S. Federal Way, P.O. Box 6  
Boise, ID 83707, USA  
Phone: (208) 363-1330  
FAX: (208) 363-2919  
E-mail:

[amihnea@micron.com](mailto:amihnea@micron.com)

### Finance Chairman:

Dr. Stephen Keeney  
M/S RN3-01, Intel  
2200 Mission College Blvd.  
Santa Clara, CA 95054, USA  
Phone: 408-765-0128  
Fax: 408-765-5775  
E-mail:

[stephen.n.keeney@intel.com](mailto:stephen.n.keeney@intel.com)

For more information, please visit the 20<sup>th</sup> IEEE NVSMW web site at:

<http://ewh.ieee.org/soc/eds/nvsmw/>