Member News....

John Liu (MC2) has been elected a Member of Royal Swedish Academy, IVA. The Royal Swedish Academy of Engineering Sciences, IVA. IVA has 842 Swedish and 260 foreign members.

Ephraim Suhir has been elected as a Foreign Full Member (Academician) of the Academy of Engineering (Technological) Sciences of Ukraine.

Chapter News....

Hu&Ro Joint CPMT Chapter Hosted Nanotechnology for Electronics EuroTraining awareness course 16, 17 & 19 Nov 2009, in Bucharest, Romania Processing techniques & tools, applications in biosensing & optics: Dr. Harry Heinzelmann, Nanotechnology & Life Sciences, CSEM Centre Suisse d'Electronique et de Microtechnique SA, Switzerland.

he IEEE-CPMT Hungary & Romania Joint Chapter hosted, organized and technically sponsored a EuroTraining awareness course on Nanotechnology for Electronics. Engineers and scientists were invited from companies, SMEs, universities and institutes to upgrade their knowledge and learn about the important topics of nanoscience from highly qualified experts.

The course focused on the most important trends of nanotechnologies according to the recommendations of **ENIAC's Roadmap of Nanoelectronics** (Figure 1). ENIAC is the industry driven European Technology Platform of Nanoelectronics, acknowledged and sponsored by the European Commission as well. It proved to be very useful that an industry survey was also carried out to define the course content for the special requirements of the New Member States (NMS) of the European Union.

The course was held in Bucharest, Romania: the first day tutorials on 16th of November, 2009, at the Politehnica University of Bucharest; and the second day technical lectures and demonstrations in two repeated cycles 17 & 19 November, 2009, at IMT-Bucharest, the National Institute for Research and Development in Microtechnologies.

The course was announced on a website dedicated to the "Nanotechnology for Electronics" course at www.ett.bme.hu/ET_NE2/, and on the EuroTraining portal (www.eurotraining.net).

The **course program** and the **lecturers** were as follows:

First day tutorials:

Introduction, theory, basic approaches and applications:

Prof. Göran Wendin, Department of Microtechnology and Nano-science,

Chalmers University of Technology, Sweden (Figure 2);

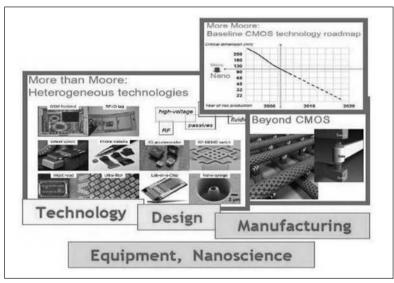


Fig. 1 Topics of the course were defined according to the ENIAC's Nanoelectronics Roadmap



Fig. 2 Prof. Göran Wendin during his lecture at the "Nanotechnology for Electronics" course in Bucharest.