



STANFORD
nanoHeat

goodson@stanford.edu
http://www.nanoheat.stanford.edu

3D Chip Stacks

Novel Thermal Interface Materials

Srilakshmi Lingamneni
sril@stanford.edu
PI: Prof. Ken Goodson
Department of Mechanical Engineering
IEEE CPMT Society
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Outline

- **Stanford Nano/Micro Heat Lab**
 - Overview of Metrology and Materials

- **Materials for Thermal Management**
 - Aligned CNT nanotape
 - High density aligned CNT composites
 - Mechanical Characterization
 - 3D chip attachments and conductive underfills

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Nano Heat Transfer Lab

Current Group

Josef Miler	Elah Bozorg-Grayeli	Woosung Park
Yuan Gao	Amy Marconnet	
Jaeho Lee	Shilpi Roy (EE)	
Sri Lingamneni	Michael Barako	Prof. Mehdi Asheghi
Saniya Leblanc	Lewis Hom	Dr. Yoonjin Won
Jungwan Cho	Zijian Li	Dr. Takashi Kodama

Alumni

Prof. Dan Fletcher	UC Berkeley	Dr. Jeremy Rowlette	Daylight Solns
Prof. Evelyn Wang	MIT	Dr. Patricia Gharagozloo	Sandia Labs
Prof. Katsuo Kurabayashi	U. Michigan	Dr. Per Sverdrup	Intel
Prof. Sungtaek Ju	UCLA	Dr. Chen Fang	Exxon-Mobile
Prof. Mehdi Asheghi	Stanford	Dr. Milnes David	IBM
Prof. Bill King	UIUC	Dr. Max Touzelbaev	AMD
Prof. Eric Pop	UIUC	Dr. Roger Flynn	Intel
Prof. Sanjiv Sinha	UIUC	Dr. Julie Steinbrenner	Xerox Parc
Prof. Xuejiao Hu	Wuhan Univ.	Dr. John Reifenberg	Alphabet Energy
Prof. Carlos Hidrovo	UT Austin	Dr. David Fogg	Creare
Prof. Kaustav Banerjee	UCSB	Dr. Matthew Panzer	KLA-Tencor
Prof. Sarah Parikh	Foothill College		
Prof. Ankur Jain	UT Arlington		

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3

Thermal and Mechanical Characterization

Cross-sectional IR Microscopy

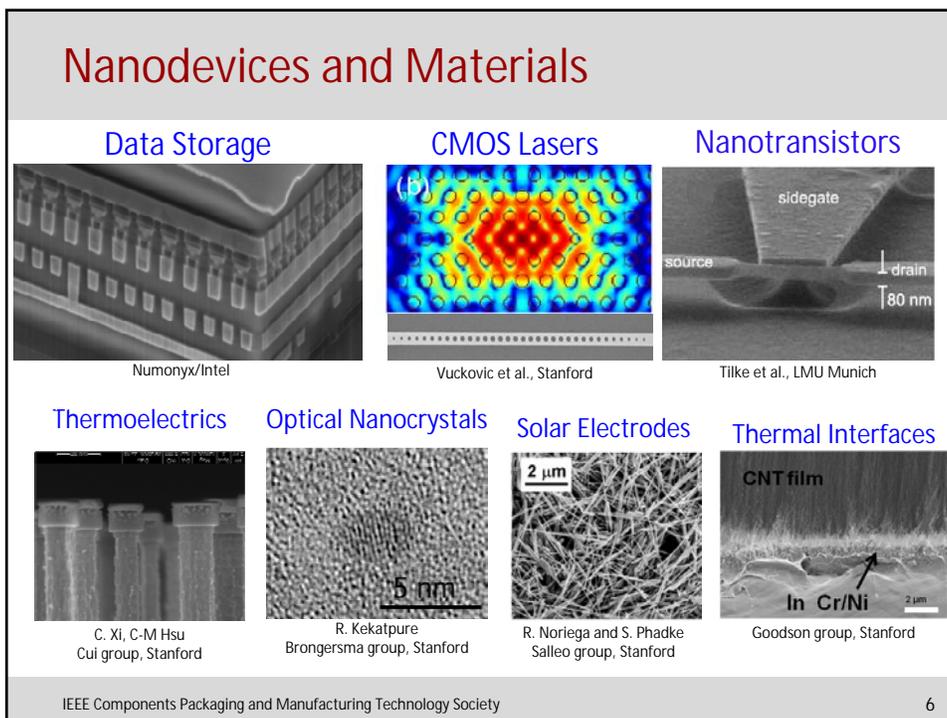
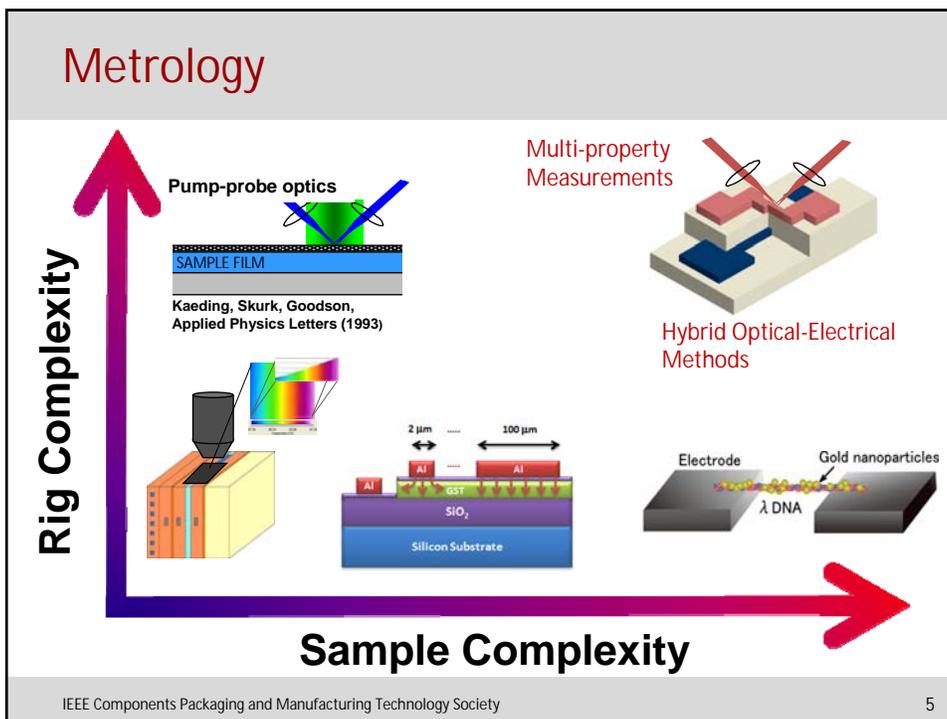
Pico/Nanosecond Thermoreflectance

Electrothermal Characterization

Mechanical Characterization

Microresonators (In-Plane)

Nanoindentation (Out-of-Plane)



Nanothermal Impact

...on Reliability (y-axis)

...on Performance (x-axis)

- nano-transistors
- 3D chip integration
- optical interconnects
- thermoelectric energy harvesting
- RF electronics
- high-density nonvolatile memory
- rapid PCR & blood analysis

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Phase Change Memory

Groups of H.S. Philip Wong (EE) and Kenneth E. Goodson (ME)

Sponsors & Collaborators: Intel (D. Kau, K-W. Chang, Ilya V Karpov, G. Spandini), NXP (F. Hurckx), Micron (John Smythe), IBM (Raoux, Krebs, et al.), National Science Foundation (NSF), Semiconductor Research Corporation (SRC)

Thermal Characterization

Jaeho Lee, Zijian Li, Elah Bozorg-Grayeli, SangBum Kim, John Reifenberg

Novel Geometries, Synthesis, & Multibit

Yuan Zhang, Rakesh Gnana, Sangbum Kim, John Reifenberg

ThermoElectric Effect (Seebeck)

Jaeho Lee, Rakesh Gnana, Zijian Li

Threshold Switching Phenomena

SangBum Kim, Rakesh Gnana, John Reifenberg, Jaeho Lee, Zijian Li

MicroThermal Stage (MTS)

Electrothermal/Crystallization Modeling

John Reifenberg, Zijian Lee

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Key Challenges for TEs in Combustion Systems

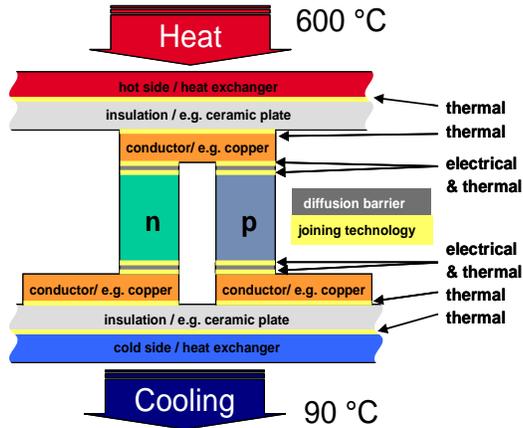
Improvements in the intrinsic ZT of TE materials are proving to be very difficult to translate into efficient, reliable power recovery systems.

Major needs include...

...Low resistance interfaces that are stable under thermal cycling.

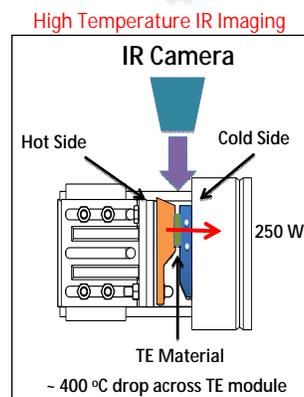
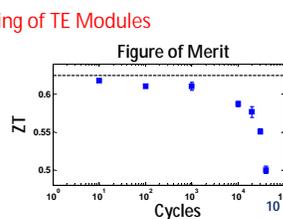
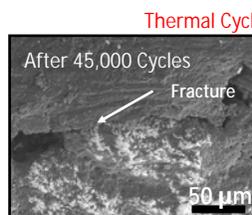
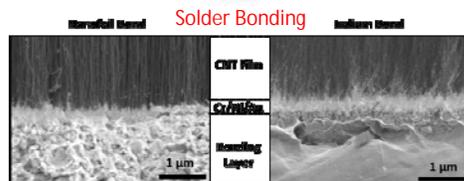
...High-temperature TE materials that are stable and promise low-cost scaleup.

...Characterization methods that include interfaces and correlate better with system performance.



Automotive Waste Heat Recovery Thermoelectric Modules and Electro-Thermo Interfaces

Michael Barako, Lewis Hom, Sania Leblanc, Yuan Gao, Woosung Park, Amir Aminfar, Amy Marconnet, Dr. Mehdi Asheghi



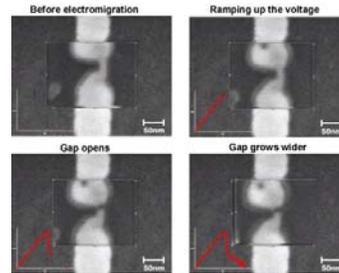
Thermal Management Challenges for Microprocessors

Importance of Hotspot Thermal Management

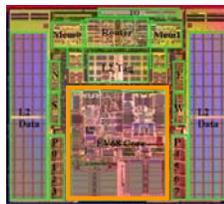
- Peak temperatures limit the reliability of interconnects
- Thermo-mechanical strains due to temperature non-uniformities can degrade packaging and interfaces

Challenges Ahead for Thermal Management

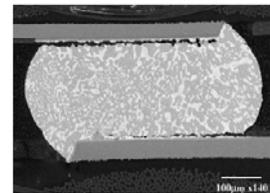
- Transistor scaling
- Increasing number of cores
- 3D integration
- Constraints of mobile applications



Images of Electromigration Failure
Ralph Group, Cornell University



Adapted from: http://en.wikipedia.org/wiki/Heat_sink

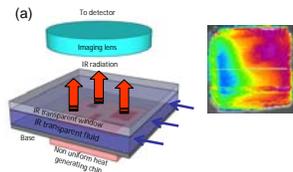


Solder joint failure due to thermomechanical stress
Ridgetop Group Inc.,

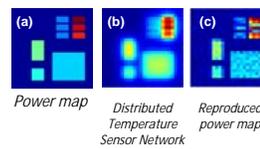
Hot Spot Detection and Thermal Management

To resolve transient chip hotspots with increased accuracy and cool them with high-heat flux cooling solutions *Milnes David, Joe Milner, Lewis Hom, Dr. Mehdi Asheghi*

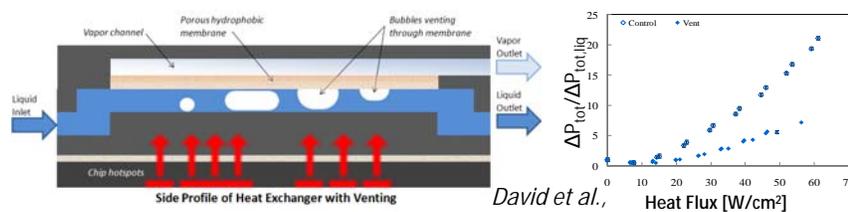
Through-Wafer Hotspot Imaging



Rapid Hotspot Prediction & Power Distribution



Vapor-Venting Microfluidic Heat Exchangers



David et al.,

Outline

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 - Mechanical Characterization – Resonator
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3D chips: Material Requirements

Goal: Discover and characterize advanced materials containing nanoscale inclusions (particles, platelets, tubes), targeting the unique property needs of packaging applications including TSV, interposer, and 3D

TIM 1 & 2 (metal alloys, particle filled organics, aligned CNT films)

- High thermal conductivity
- Mechanical compliance

3D Chip Attachment (Adhesives, Thermal compression bonding)

- High thermal conductivity
- Electrically insulating
- Thermal cycling stability

Encapsulation

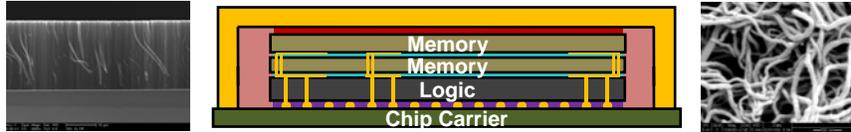
- High thermal conductivity
- Electrically insulating (on the side facing the chip)
- Mechanical compliance

Flowable Underfill

- Electrically insulating
- Mechanical stiffness
- Viscosity and capillary forces
- High thermal conductivity

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Challenges Posed by Material Requirements

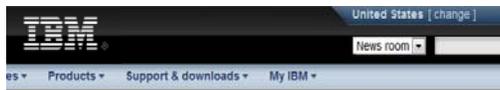


- Problem: **Heat removal from 3D stacked circuits** is severely limited by low conductivities of the underfill/BGA and the thermal interface.
- Solution: **Nanostructured organics** promise high thermal conductivity with the mechanical and viscoelastic properties required for manufacturing and lifetime reliability.

- | | | |
|---|--|--|
| <ul style="list-style-type: none"> ➤ Application: <ul style="list-style-type: none"> • Passivation layer during the BEP of the wafer • Non-conductive adhesive as a replacement to under-fill | | <p>Challenges:</p> <ul style="list-style-type: none"> • Maintaining nanostructure during dispensing and CMP • Minimizing negative impact from contacting the nanostructure to the adjacent materials • Impact of thermal pressure bonding during assembly process • Thermo-mechanical stresses between the polymer and the micro-bump/Cu pillar • Effects of reflow process |
|---|--|--|

IBM and 3M collaboration

High thermal conductivity underfill adhesives for building silicon skyscrapers

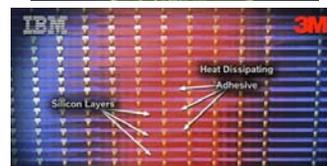
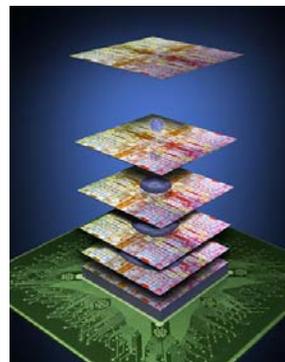


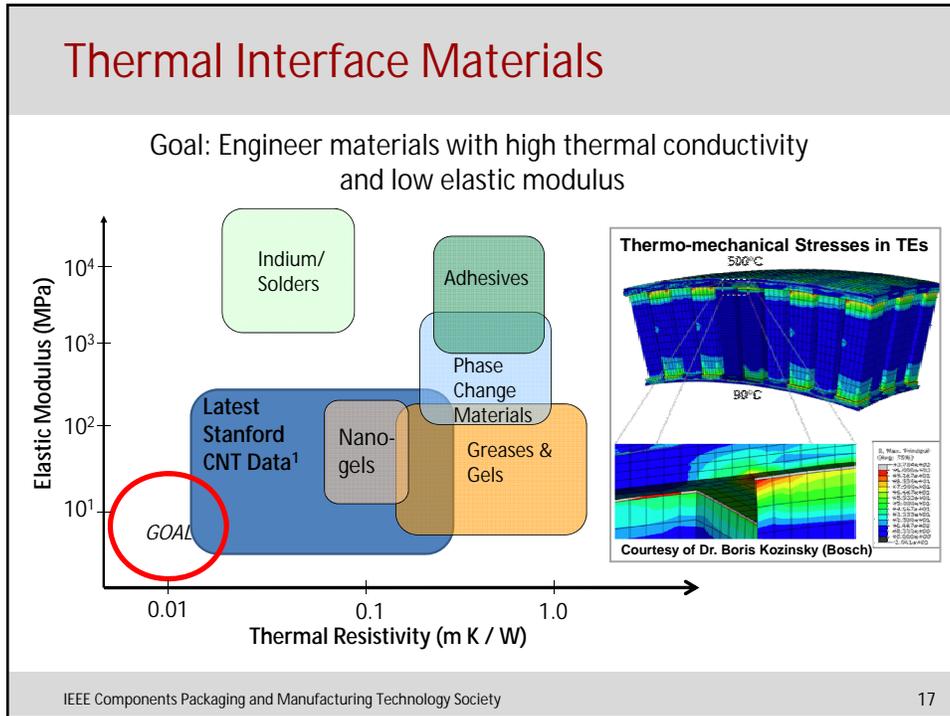
3M and IBM to Develop New Types of Adhesives to Create 3D Semiconductors

Innovation leading to the creation of 'Silicon Skyscrapers'

- | | |
|---|---|
| <ul style="list-style-type: none"> ↓ News release ↓ Related XML feeds | <ul style="list-style-type: none"> ↓ Contact(s) information ↓ Related resources |
|---|---|

ST. PAUL, Minn. & ARMONK, N.Y. - 07 Sep 2011: 3M and IBM (NYSE: [IBM](#)) today announced that the two companies plan to jointly develop the first adhesives that can be used to package semiconductors into densely stacked silicon 'towers.' The companies are aiming to create a new class of materials, which will make it possible to build, for the first time, commercial microprocessors composed of layers of up to 100 separate chips.





CNT Die Attachment

Carbon 2012

Mechanical characterization of aligned multi-walled carbon nanotube films using microfabricated resonators

Yoonjin Won ^{a,*}, Yuan Gao ^a, Matthew A. Panzer ^a, Senyo Dogbe ^b, Lawrence Pan ^c, Thomas W. Kenny ^a, Kenneth E. Goodson ^a

Temperature-Dependent Phonon Conduction and Nanotube Engagement in Metalized Single Wall Carbon Nanotube Films 2010

Matthew A. Panzer, [†] Hai M. Duong, [‡] Jun Okawa, [§] Junichiro Shiomi, [§] Brian L. Wardle, [†] Shigeo Maruyama, [†] and Kenneth E. Goodson ^{†,*}

NANO LETTERS

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Nanotape to Replace Solder Pads

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DESIGN STRATEGIES FOR ARM SYSTEMS
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News & Analysis

Nanotape could make solder pads obsolete
R. Colin Johnson
12/4/2011 12:01 AM EST
PORTLAND, Ore.—Solder pads could soon be made obsolete by a new nanotape material created by the Semiconductor Research Corporation and Stanford University.
By sandwiching thermally conductive carbon nanotubes between thin

SRC Patent: Hu, Jiang, Goodson, US Patent 7,504,453, issued 2009
SRC Patent: Panzer, Goodson, et al., 2009/0068387 (pending)
Panzer, Maruyama, Goodson et al., Nanoletters (2010)
Hu, Fisher, Goodson et al., J. Heat Transfer (2006)

Adhesion layer wets nanotubes and promotes adhesion of binder (Pd, Pt, or Ti).

Low melting temperature binder (e.g. alloys of Ga, In, Sn)

Adhesion layer

Nanofibers

Removable mechanical backer

~100 nm is the typical variation in CNT height.

Upon heating, the low melting binder conforms to CNT and substrate topography.

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Mechanical Properties of CNT Films New Technique: Mechanical Resonators

Thermal and Mechanical Characterization

Pump
Probe
Laser Doppler Vibrometer (LDV)
Piezoelectric Shaker

Resonator length and shape variation

CNT on a Cantilever

Experimental Setup

LDV
Device
Piezoelectric Shaker
Vacuum Chamber
HP 89411A
GPIB
PC

- LDV (laser Doppler velocimetry) experimental setup : resonant frequency of various thickness films.
- Resonant frequency shift : mechanical modulus
- Ring-down and fitting measurements : quality factors

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Experimental Method and Data Interpretation

Euler-Bernoulli differential equation for multi-layer beam

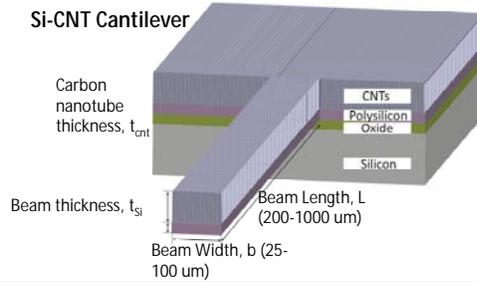
$$\overline{\rho A} \frac{\partial^2 w}{\partial t^2} + \overline{EI} \frac{\partial^4 w}{\partial x^4} = 0 \quad \overline{\rho A} = \sum_i \rho_i A_i \quad \overline{EI} = \sum_i E_i I_i$$

Transformed section method

$$\frac{\Delta w_n}{w_{n,0}} = \sqrt{\frac{E_{Si} I_{Si} + E_{CNT} I_{CNT}}{\rho_{Si} A_{Si} + \rho_{CNT} A_{CNT}}} \sqrt{\frac{\rho_{Si} A_{Si}}{E_{Si} I_{Si,0}}} - 1$$

$$E_{CNT} = \frac{E_{Si}}{I_{CNT}} \left(\left(1 + \frac{\rho_{CNT} A_{CNT}}{\rho_{Si} A_{Si}} \right) \left(1 + \left(\frac{\Delta w_n}{w_{n,0}} \right)^2 \right) I_{Si,0} - I_{Si} \right)$$

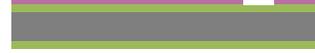
Si-CNT Cantilever



Polysilicon deposition



Resonator outline etching



Resonator etching



Oxide layer removal



Catalyst deposition

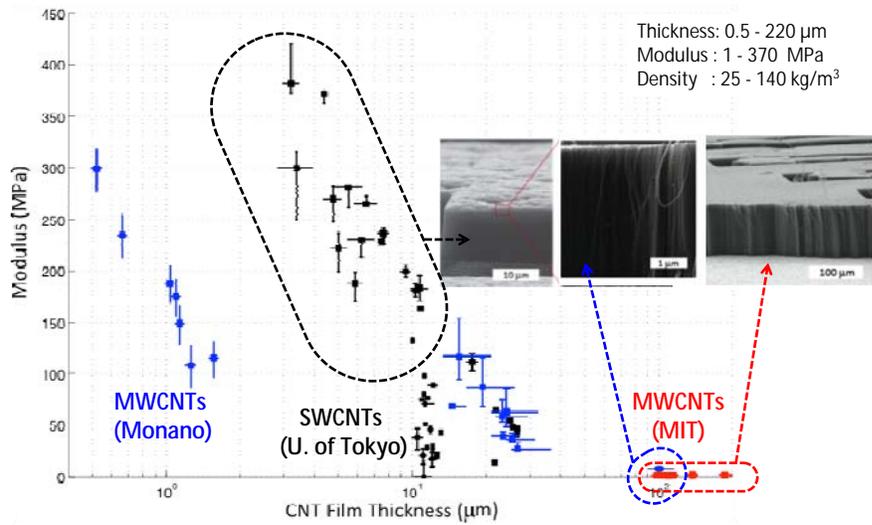


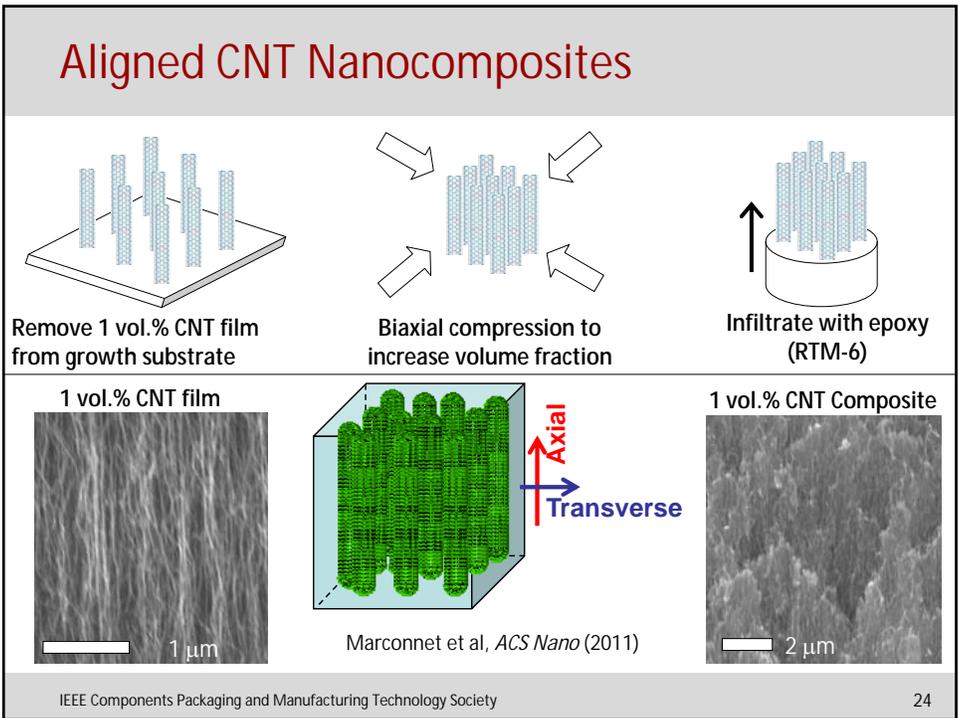
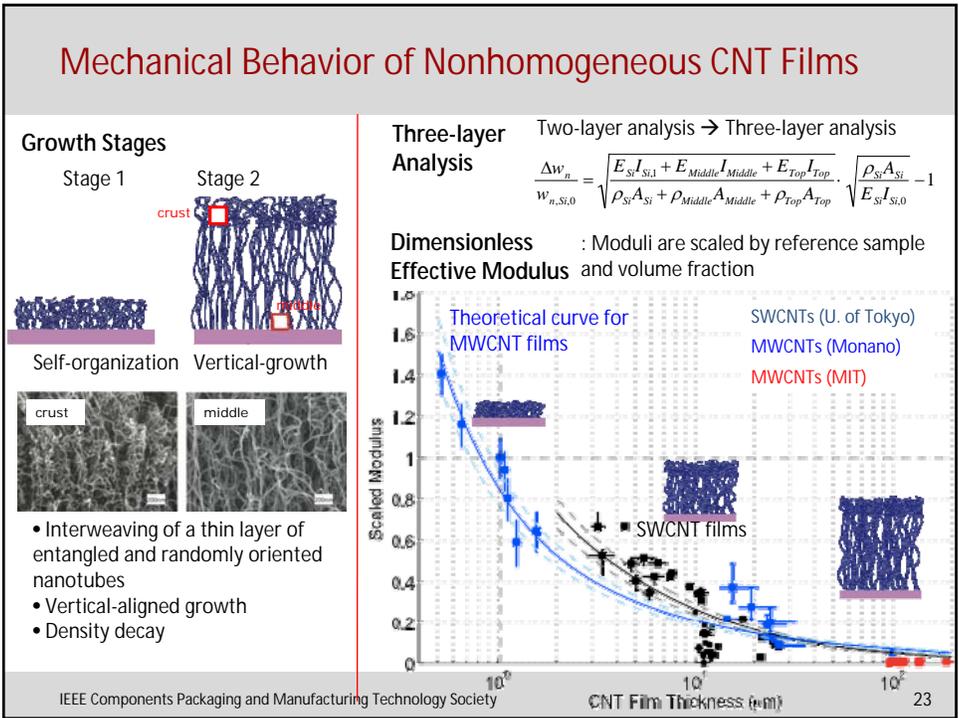
Carbon Nanotube Growth

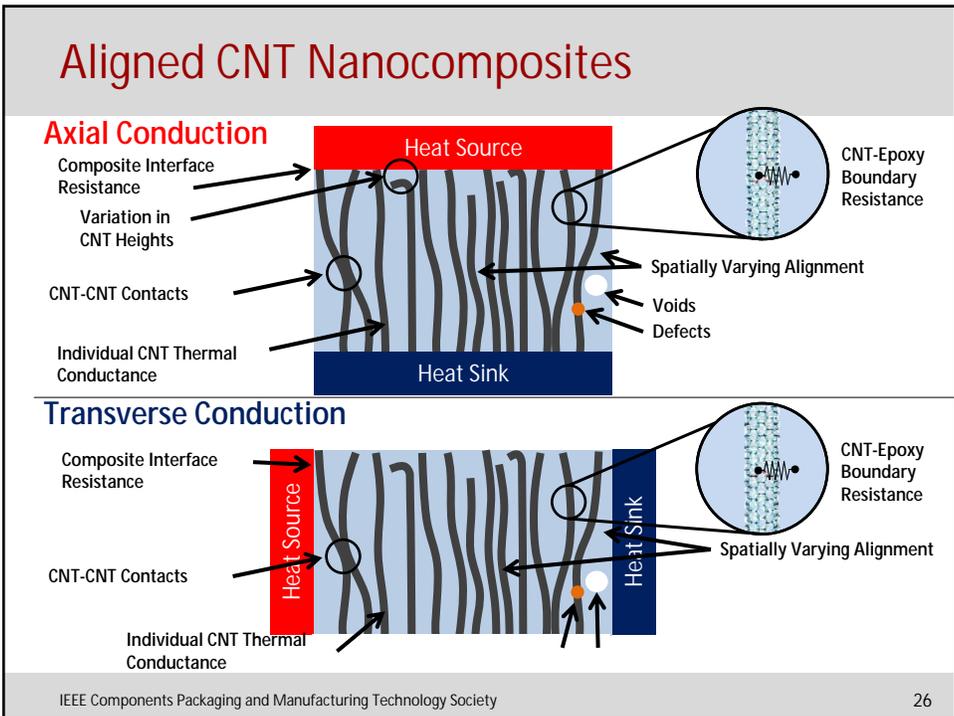
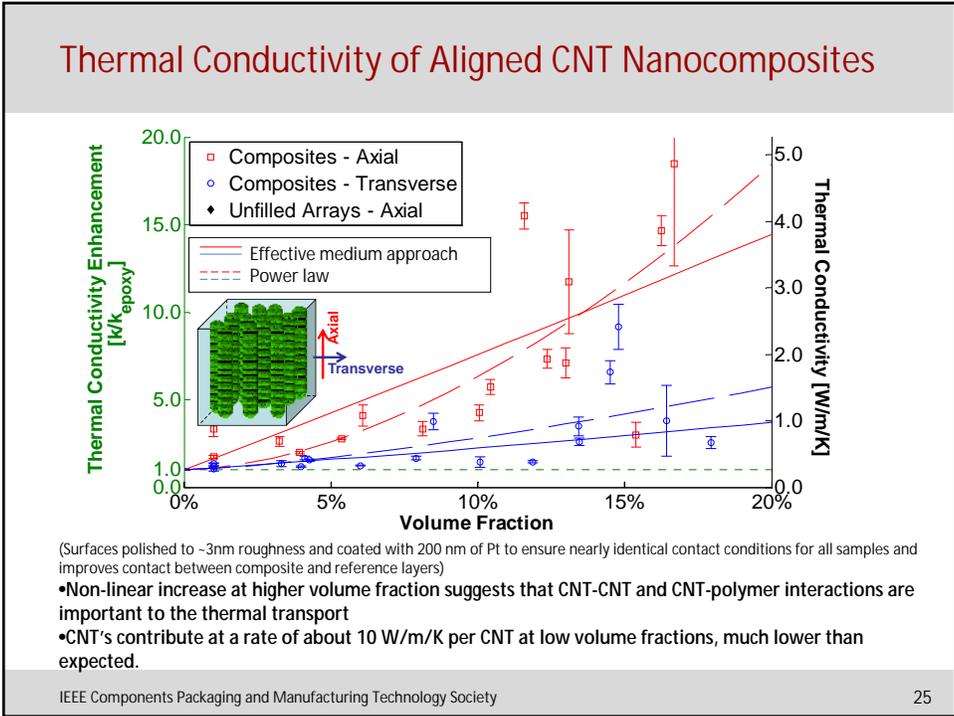


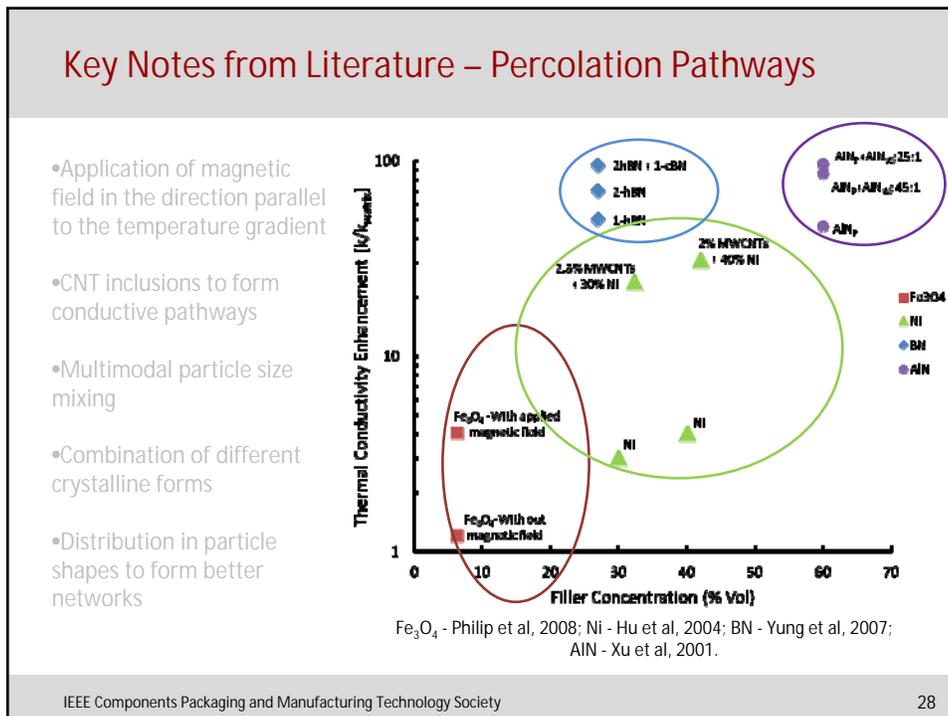
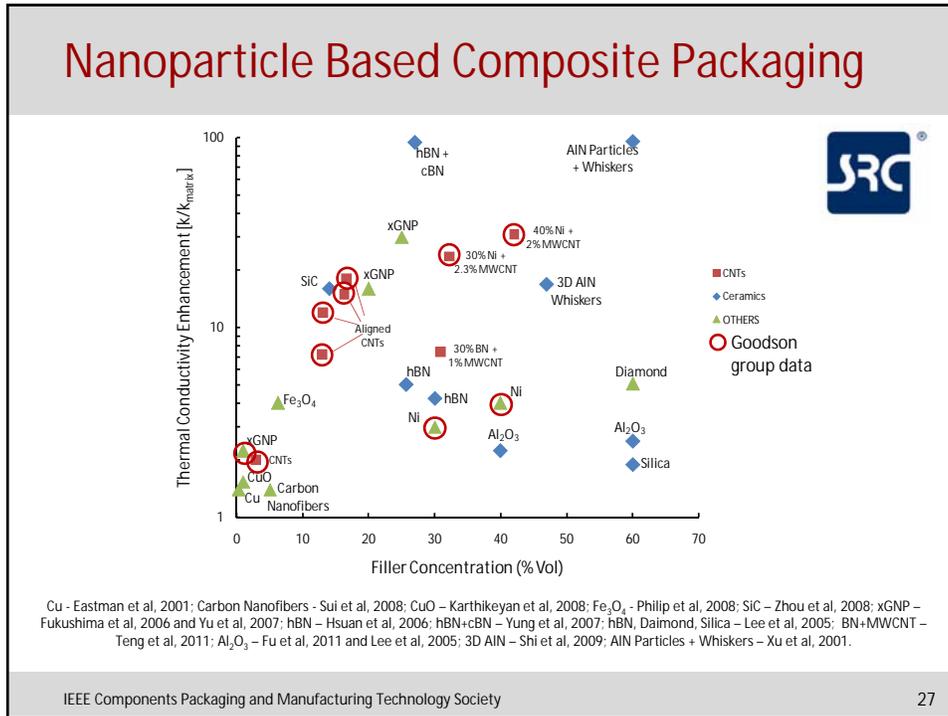
Won et al, *Carbon* (2011)

Mechanical Behavior of CNT Films



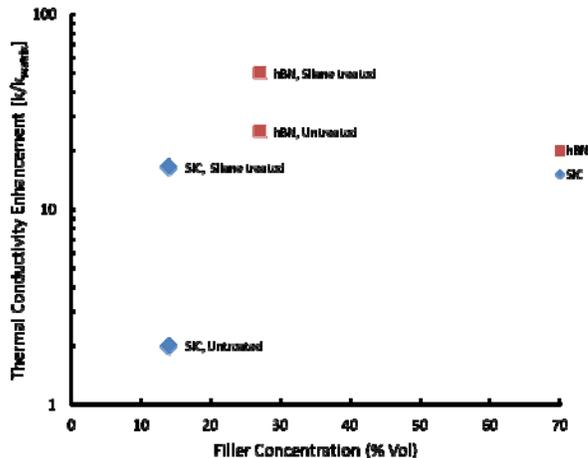






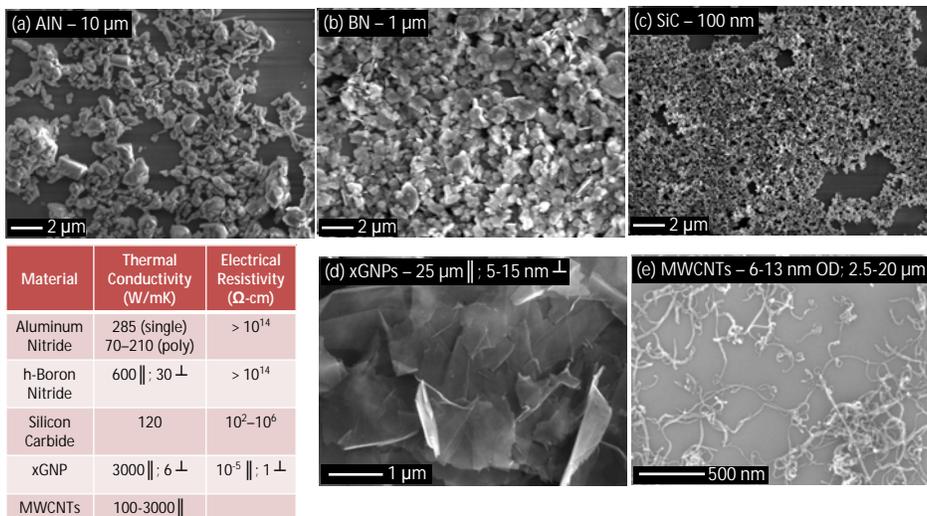
Key Notes from Literature – Chemical Treatment

Silane surface treatment of particles to form particle-resin interface structures



SiC – Zhou et al, 2008; hBN - Yung et al, 2007.

Candidate Filler Micro/Nano Particles



Preliminary Thermal Conductivity Data

Samples: particles dispersed in silicone oil
Technique : IR imaging

- 1% xGNP performed better than 1% MWCNT
- 1% xGNP and 1% MWCNT performed better than 10% AIN
- 1% xGNP addition to 10% AIN showed promising enhancement

Particle Composition (Vol %)	Thermal Conductivity Enhancement (k/k_{matrix})
1 % xGNP	2.1
1% MWCNT	1.5
10% AIN	1.3
10% AIN + 1% xGNP	2.8

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31

Ongoing Work and Future Directions

- Ongoing work
 - Cured polymer nanocomposites
 - Various surface treatments of filler particles
 - Electrical characterization of polymer nanocomposites
 - Compare thermal and electrical conductivity data against existing effective medium theories
- Future directions
 - Bonding of composite materials to Si
 - CTE measurement of composite materials bonded to mechanical resonator
 - Real-time evolution of interfacial adhesion, fatigue, debonding

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32

Conclusions - Materials for Thermal Management

- Development of novel thermal interface materials is crucial for 3D circuits performance
- Nano tape is a promising replacement to solder pads
- Measurements of aligned CNT films and composites showed thermal conductivity and elastic modulus comparable to or better than commercial TIMs
- Preliminary thermal conductivity data of nanosuspensions in silicone oil showed promising trends, and this work is being extended to nanocomposites

Questions & Comments