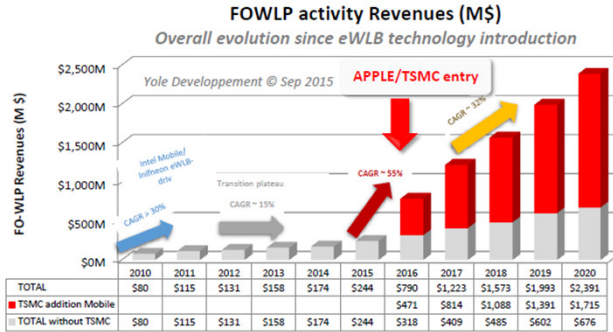




FOWLP Potential



- Entry of A10 APE of iPhone7, 7+ and newer from 2016!
- Previous 2014-2020 CAGR was rated at 25%, while new 2014-2020 CAGR is estimated at 55%!
- After the jump, further 2016-2020 CAGR estimated at 32%
- Market estimated to exceed 2B\$ by 2020



From TECHNOLOGIES to MARKET

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Why Large Format Packaging (LFP) Will Become Increasing Important ?

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Drivers of Large Format Packaging (LFP)

- Value Adding
 - More than Moore
 - Package level system Integration
 - less board space required
- Handling critical quartet of APT
 - Performance
 - Form factor
 - Reliable
 - Cost
- 4th Wave of SC
 - Internet of Things
- Big Data

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Advanced Packaging Platforms

Integration: 2D → 3D

	Leadframes	w/o IC substrates	IC substrates-based				Embedded SiP
Multiple Dies							
Single die	QFN & MIS	Fan-in	Fan-out	W/B BGA	Flip Chip BGA	BGA (organic substrate)	Embedded die (in substrate)

← **Advanced SiP** →
 (QFN/MIS, WLP and Laminate based)

Complexity: → Increased functionality, I/Os, integration complexity

Source: Yole **“SiPlization”**

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ASM Total Solution for WLP & PLP Technology

1	2	3	4	5
Pick n Place	Molding	Ball Drop	Singulation	WLP Inspection, Test & Packing

 Precision WLP & PLP NUCLEUS	 ORCAS	 DEK GALAXY	 LASER1205	 SUNBIRD
 WLP & Large PLP SIPLACE CA				

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5S/6S protection to prevent electrical short for **small discrete**

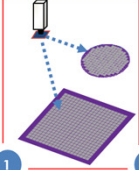
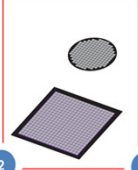
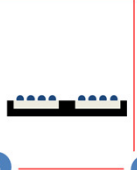
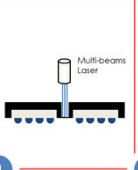
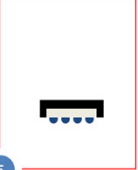




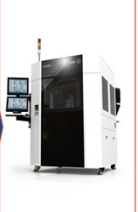
Electrical Short

No Short

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FLOWline™ for LFP 1/


 <p>1</p>	 <p>2</p>	 <p>3</p>	 <p>4</p>	 <p>5</p>
Pick n Place	Molding	Ball Drop	Singulation	WLP Inspection, Test & Packing
 <p>Precision WLP & PLP NUCLEUS</p>	 <p>ORCAS</p>	 <p>DEK GALAXY</p>	 <p>LASER1205</p>	 <p>SUNBIRD</p>
WLP & Large PLP SIPLACE CA				

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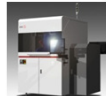
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FLOWline™ for 5S/6S

Process :



8" / 12" wafer

- 1) Ball formation by Ball Drop or Paste Printing (Dek) 
- 2) Wafer Pre-cut by blade or Grooving by laser (ALSI) 
- 3) Front & Back sides wafer (8" & 12") level molding (ORCAS) 
- 4) Final package singulation with mold compound remain at the package edges for protection (Laser 1205) 
- 5) Wafer level test / finishing to T&R with 6 sides quality inspection (Sunbird) 

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Concept of CCCV 1/

- Cost effective design, Cost effective solution, Cost-to-deliver Value for customers.
- Critical quartet of CCCV are:
 - Multi-functional with increase performance
 - Reliability
 - Form factor,
 - Cost efficiency

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Concept of CCCV 2/

Clamping edge

Mold cap
Substrate

Keep-out-zone (KOZ) product

Cap shoulder

Mold cap
Substrate

Over-mold (Over Size molding) product

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Flexibility of Mold Tooling

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Compression Molding Concept: Die Up/Down/KoZ/Over Mold

The diagrams illustrate four compression molding concepts:


- Die Up type:** Shows a top plunger compressing a substrate/carrier on a bottom cavity, with a bottom chase below. A 'Relax' label indicates the state after compression.
- Die Down type:** Shows a top chase and top cavity compressing a substrate/carrier on a bottom plunger, with a bottom chase below.
- Keep-out-zone (KOZ) method:** Shows a top chase, molding compound, substrate, and bottom chase. A red circle highlights the edge of the molding compound. The resulting package shows a 'Wafer Edge' where the mold cap area is greater than the substrate area.
- Over-mold method:** Shows a top chase, molding compound, substrate, and bottom chase. A red circle highlights the edge of the molding compound. The resulting package shows a 'Water Edge' where the mold cap area is less than the substrate area.

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KoZ & Overmold Molding



KOZ molding



Overmolding

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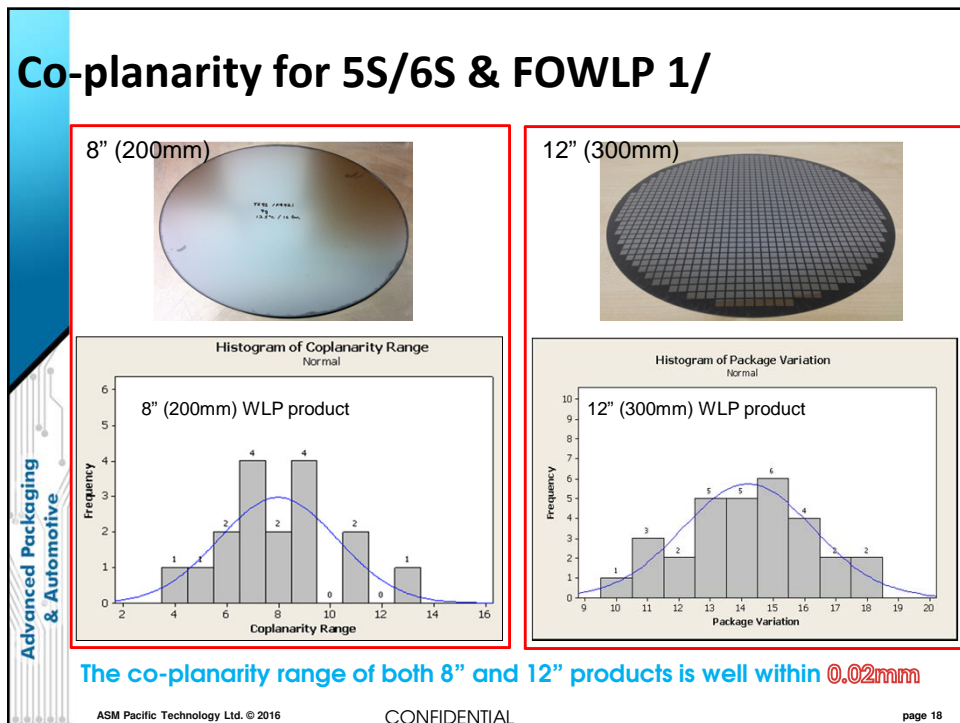
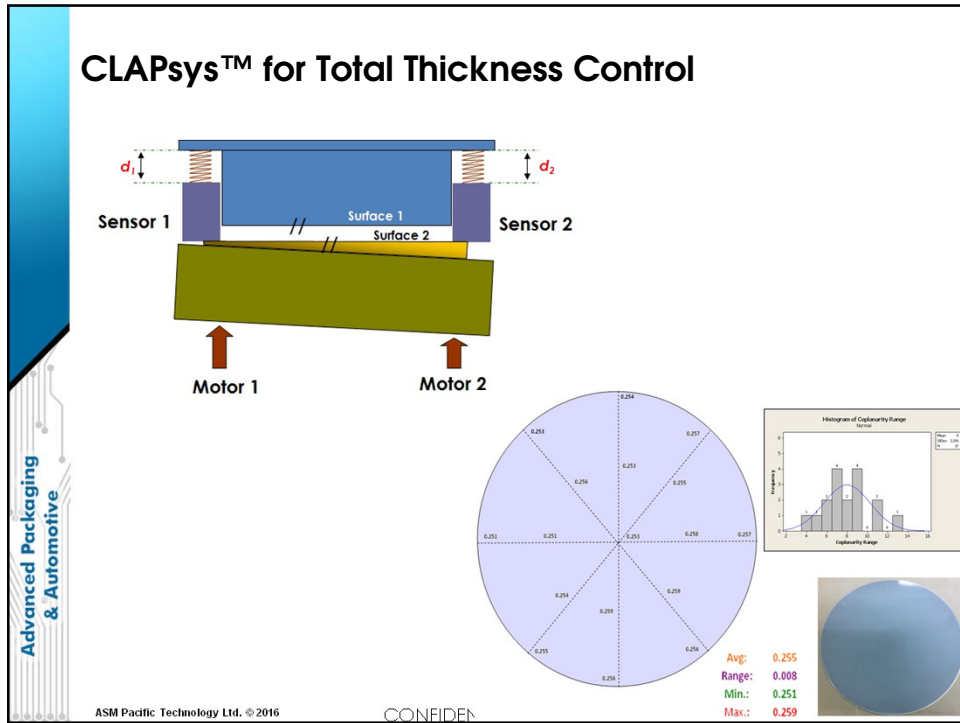
Co-planarity

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Warpage Management Discussion

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Warpage Management 1/

- Encapsulant Design
 - Filler Loading
 - Filler Size
 - CTE α_1/α_2


Package Type	Filler content (%)	CTE α_1 (ppm/°C)
1. Standard	~89	~7.5
2. Thin package	~87	~7.5
3. Standard	~72	~20
4. Low warpage	~80	~14
5. Discrete	~78	~14
6. COW/TSV/Glass	~86	~10

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Warpage Management 2/

- Encapsulant Design


Substrate With No Feature Below X-Y Plane




Substrate With Feature Below X-Y Plane



Substrate With No Feature Below X-Y Plane



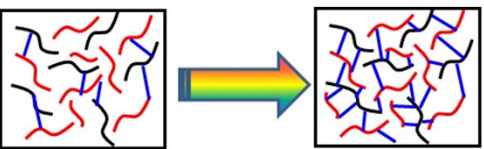
Substrate With Feature Below X-Y Plane



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Warpage Management 3/

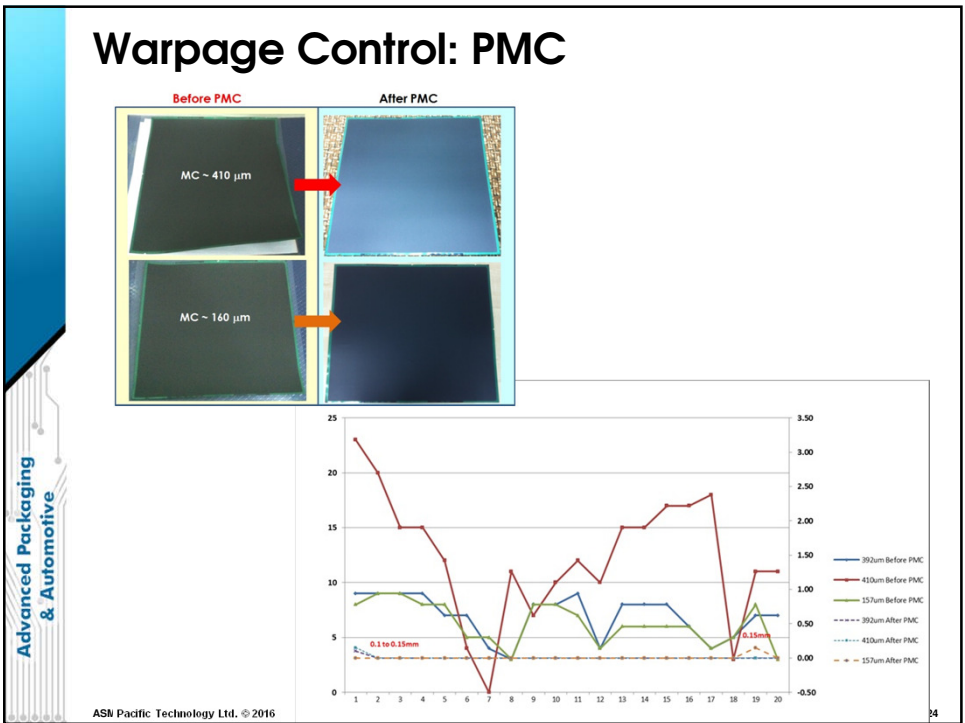
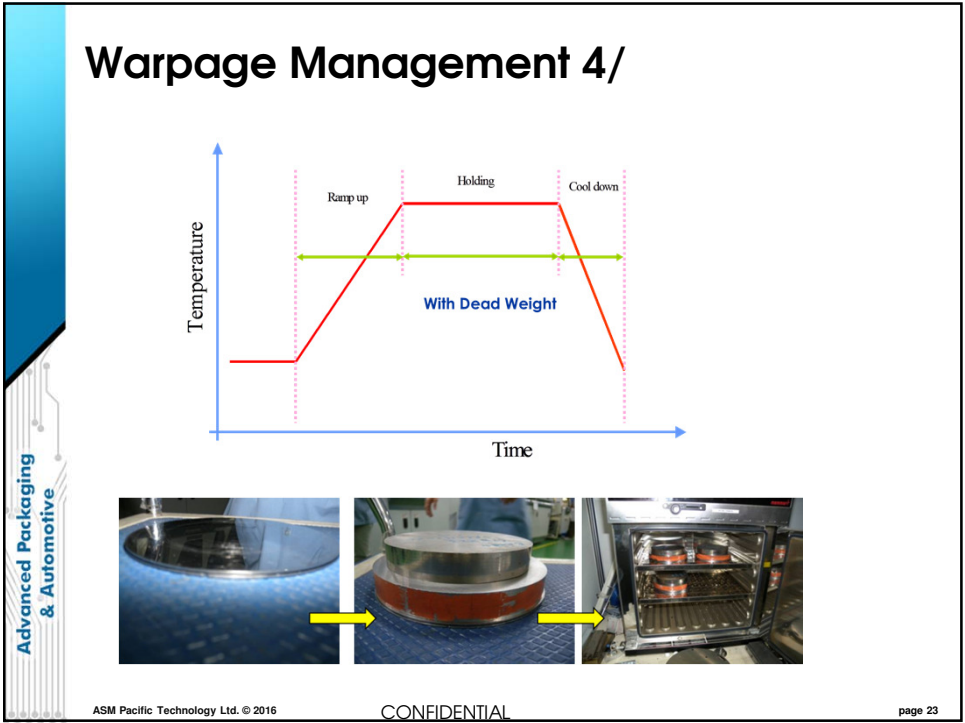
- PMC Profiling
 - Ramp Rate
 - Hold Period
 - Cooling Rate
- Dead weight
 - Physical dead
 - Spring load
 - Interleaf



**Encapsulant
Cure State After
Molding**

**Encapsulant Cure
State After Post
Mold Curing**

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Warpage of KOZ Molding

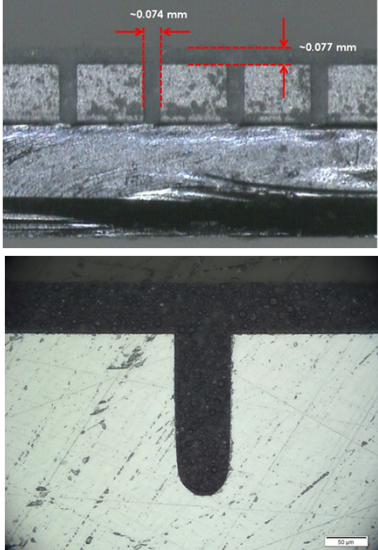


Warpage immediately after molding.

Thin Mold Cap and Dispensing

Thin Mold Cap

- Definition for our work:
 - Mold cap thickness << 200 μm
- Challenges:
 - Granular Vs Liquid
 - Cover of encapsulant over the substrate
 - Flow Mark
 - Dispensing quantity
 - Trench depth



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Thin Mold Cap: Type of Encapsulant



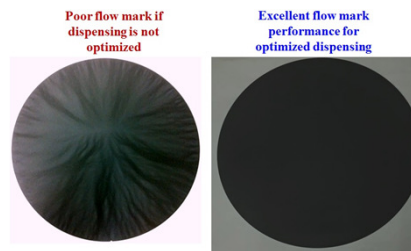
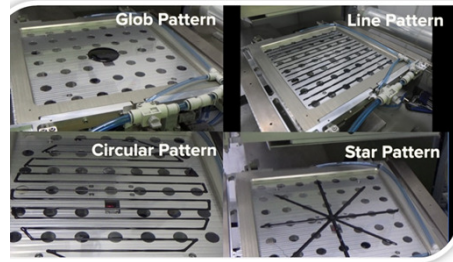
Serious Flow Mark

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Dispensing: Factors 1/

- ▶ Dispensing Patterns Definition:
 - Glob
 - Line
 - Circular (or serpentine)
 - Star
- ▶ When to apply such patterns
 - White space
 - Trench depth
 - Complexity of package
 - Mold cap thickness



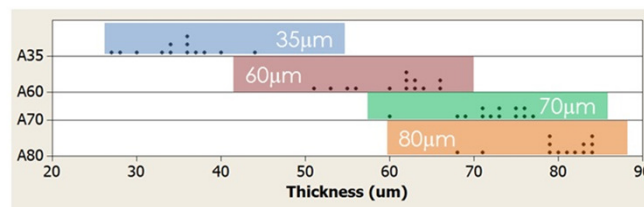
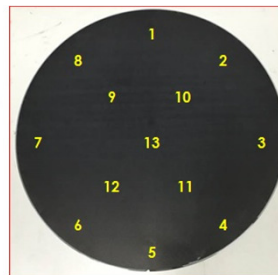
Advanced Dispensing

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Dispensing: Results 2/

Co-planarity

Ø8" WLP



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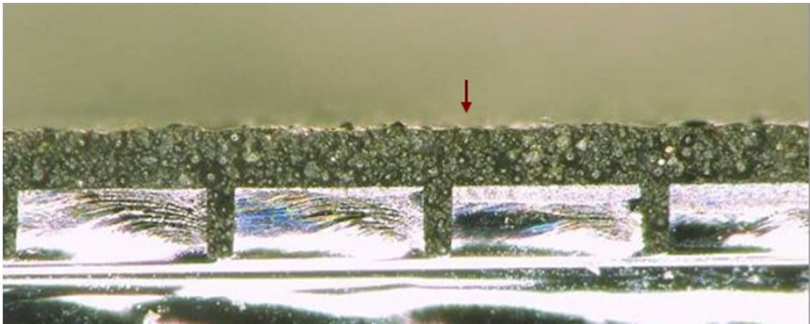
Other Moldability

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Delamination

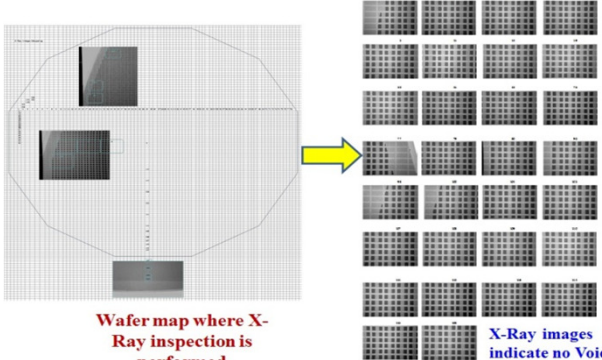
- Factor to consider before, during and after mold
 - pre-treatment, molding process and de-molding



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Voiding

- Factor to consider before and during molding
 - Tool design, pre-treatment, and molding process
 - Air Vent
 - Mini Vacuum Chamber
 - Vacuum is Key Points



Wafer map where X-Ray inspection is performed

X-Ray images indicate no Voids

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Summary of Work

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Summary of Work 1/

- Large format packaging is a cost-effective packaging solution to produce encapsulated 5S or 6S packages.
- Achieving TTV of 20 μm of co-planarity for LFP is possible with closed loop automated co-planarity monitoring within the encapsulation system.
- Key factors for warpage control are:
 - Encapsulant design
 - Post mold cure with profiling and dead weight
 - Ratio of Encapsulant versus Silicon

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Summary of Work 2/

- Thin mold cap of $\ll 200 \mu\text{m}$ is achievable via:
 - Suitable liquid encapsulant
 - Different variant of dispensing pattern
- Delamination and voiding is controllable with proper before and during molding.
 - Plasma cleaning
 - Pre-baking
 - Optimal molding process control
- Encapsulation System is available for mWLCSP 5S/6S

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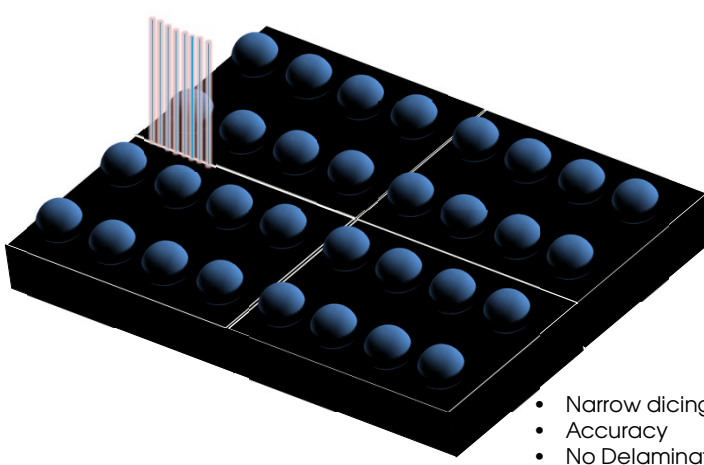
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Laser Dicing for mWLCP

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Multi Beam Laser Dicing of Molded WLCSP



The diagram shows a 3D perspective of a black rectangular substrate with a grid of blue spherical components. A vertical array of red laser beams is shown cutting through the substrate between the components.

- Narrow dicing width
- Accuracy
- No Delamination
- Fast
- Reliable

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Laser Versus Mechanical Blade Dicing

Mechanical blade

Laser

- Too wide
- High blade wear
- Chip-out
- Delamination
- Reliability
- Slow

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Multiple Beam Machine

Patented Multi Laser Beam

Laser

LBD Optics
(Laser Beam Delivery)

DOE – Diffractive Optical Element

Focussing device

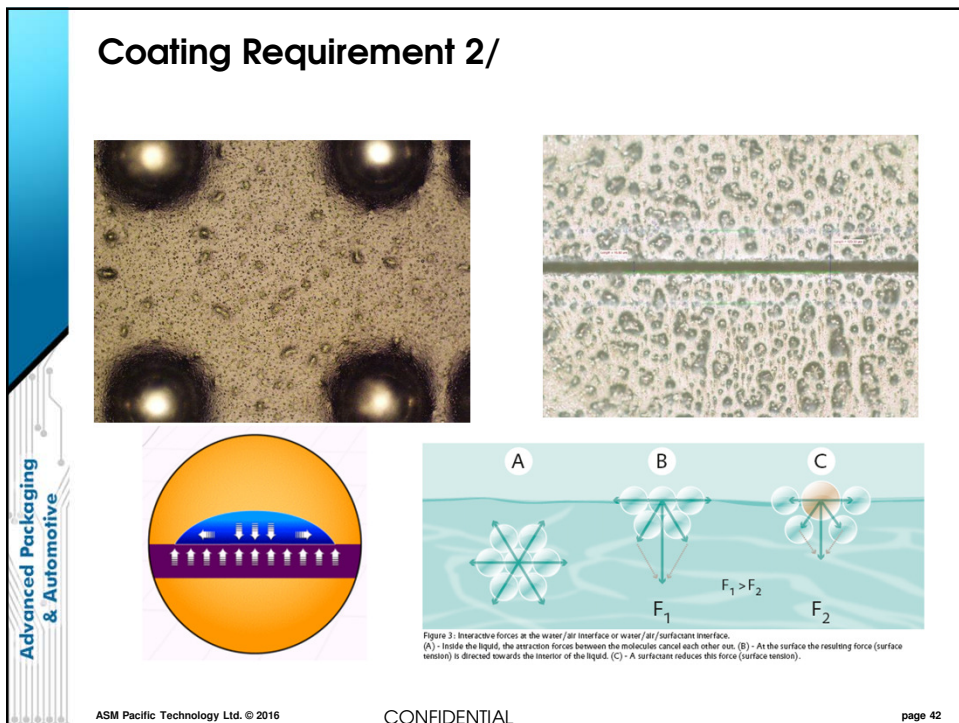
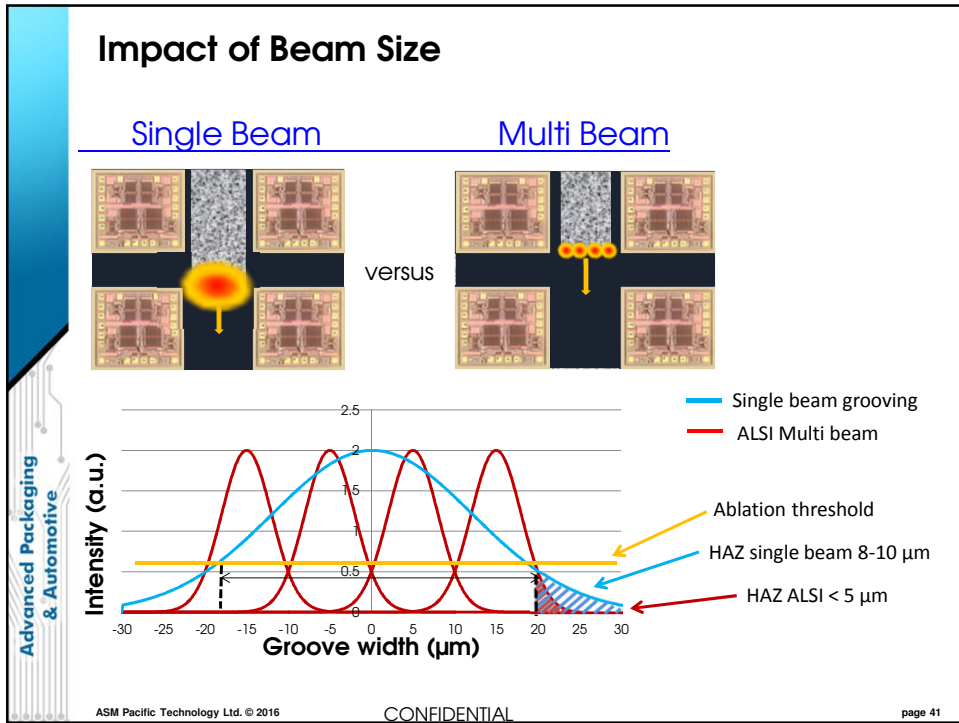
300 mm wafer stage

Active Mounts for Vibration Compensation (IP propriety)

Beams	: > 2
Distance	: 10 - 1000 μm
\varnothing Beam	: 8 μm
Accuracy	: 1.5 μm (Left and Right)

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Wafer Edge Considerations

- Manufacturing (Expose Edge)
 1. Mechanical trim after Molding or.....
 2. Molding with a Keep Out Zone (KOZ)
- Alignment on the Blade Half Cuts that are now exposed on edge

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Keep it out Zone (KOZ) molding

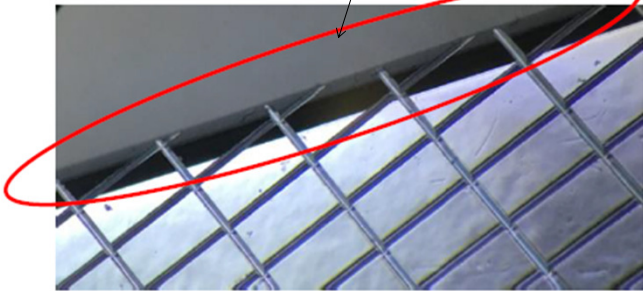
Over-mold tooling

KOZ tooling

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Exposed Edge

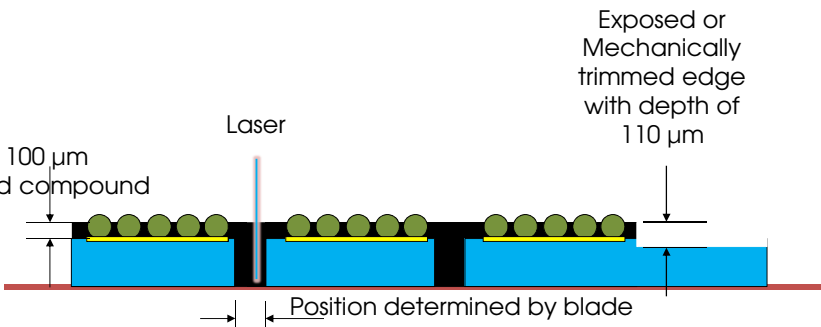
Expose the edge for future alignment



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Requirement for New Alignment Methodology



70 – 100 μm mold compound

Laser

Exposed or Mechanically trimmed edge with depth of 110 μm

Position determined by blade

Challenges:

- Dicing street is not visible (70 – 100 μm mold compound is not transparent)
- Bumps placement does not have good accuracy 2 - 3 μm accuracy

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Off Center can cause Delamination

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Edge alignment on Half Cut Lanes Blade Diced

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Figure 1: Bumps up image on dicing machine

Figure 2: Image captured on the wafer edge with dicing machine.

Edge trajectory

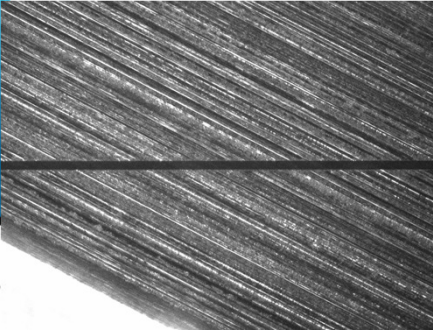
Alignment needs to look at all lanes on edge of Wafer

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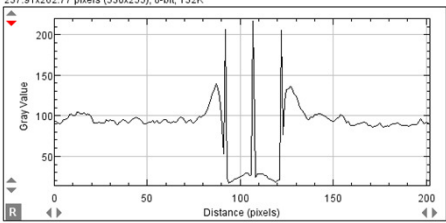
page 48

Edge alignment (Close-up)



Width: 43.1 [um]
Shift: 0.7 [um]
Result: 0
Detection mode: 11

Alignment algorithm processes the image and identifies the correction

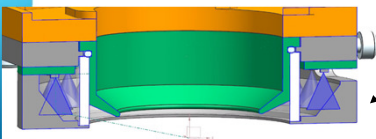


Edge alignment image captured around the perimeter of the wafer

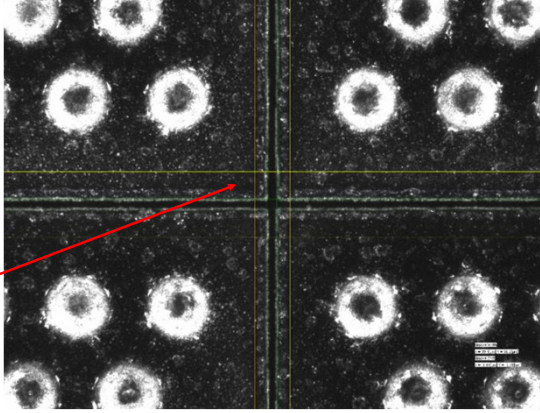
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Need Sufficient Lighting (Ring-led) for Kerf Check



Ring LED Side Light with Diffuser



Align on Kerf

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Ideal Dicing Kerf

The diagram shows a cross-section of two semiconductor dies (blue) with a thin layer of solder (yellow) on top. Green circles represent solder balls. A vertical line indicates the dicing kerf, which is labeled '1. narrow'. The cut is clean and straight, with no material loss or damage to the dies.

- Straight
- No chip-out
- No delamination
- Passes reliability tests
- Productivity

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Chip-out Is A Challenge for Laser Dicing

The diagram shows a cross-section of two semiconductor dies (blue) with a thin layer of solder (yellow) on top. Green circles represent solder balls. A vertical line indicates the dicing kerf, which is labeled '1. narrow'. The cut is not clean, and a white, irregular shape (chip-out) is visible at the bottom of the kerf, indicating material loss during the dicing process.

Chip-out strongly depends on filler size
Larger the filler size, larger the mouse bite,
e.g. Chip -out is equal to half diameter of
Filler Fused silica

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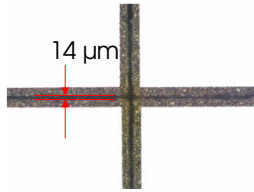
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Result of Laser Dicing 2/

Results

- Dicing width: 14 μm
- Mold compound remain: 19 μm
- Depth: 180 μm
- Productivity: 1.5 wafers per hour
- Average fillers 15 μm , max. up to 30 μm



14 μm

Figure 1: Microscope image focused on the top showing the dicing width and crossing

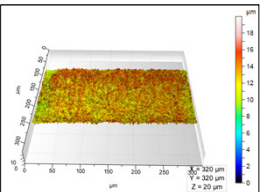
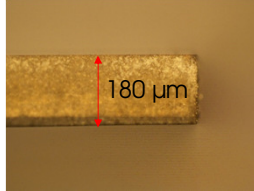


Figure 2: Confocal image focused on the sidewall

Process parameters

- $\lambda = 355 \text{ nm}$
- Passes= 2 passes
- Laser power = 3.3 to 3.8W
- Speed = 90 mm/s up to 300mm/s
- DOE: 16UV20



180 μm

Figure 3: Microscope image focused on the sidewall.

200 mm 180 μm thick mold compound
Die pitch: 628 μm x 328 μm

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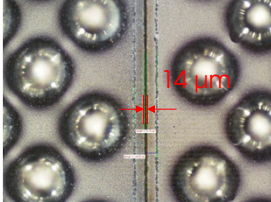
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Result of Laser Dicing 3/

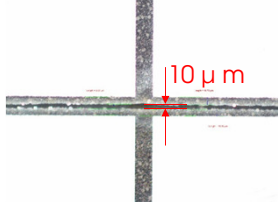
Results

- Dicing width: 14 μm
- Productivity: 1.8 wafers per hour



14 μm

Figure 1: Microscope image focused on the top side showing the dicing width

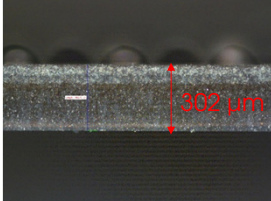


10 μm

Figure 2: Microscope image focused on the back side showing the dicing width

Process parameters

- $\lambda = 355 \text{ nm}$
- Passes: 10 passes
- Laser power: 4/3.5/3.5 W
- Speed: 67 mm/s up to 450 mm/s
- DOE: 12UV50



332 μm

Figure 3: Microscope image focused on the sidewall

300 mm 300 μm thick mold compound
Die pitch: 6198 μm x 5953 μm

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Attainable Ra For Diced Package Side Wall

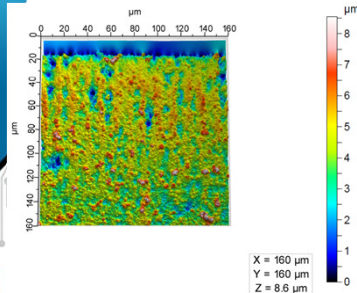


Figure 7: Confocal image on sidewall

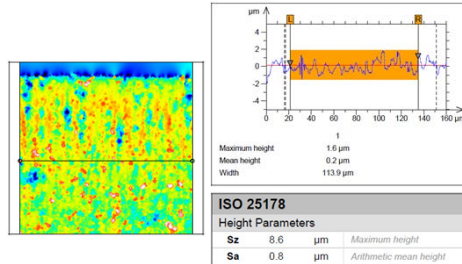


Figure 8: Average sidewall roughness < 1 micrometers

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WPH Comparison Between Competing Dicing Technology

	ASMPT	SAW
Dicing Width (um)	18	>20
UPH * (WPH)	2.7	1
Cost (\$/wafer)	12	20

■ Separation technology comparison for m-WLCSP

- Mold Cap thickness <350um on a SQ 2.8mm die size on a Ø 300mm wafer.

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Summary of Work

- This work had used following optimized processes to dice through mWLCSP package:
 - Multi Beam Laser Dicing for faster dicing
 - Special optics (Multiple Beams) to reduce Kerf Width
 - Flexibility to use different Optics to increase dicing speeds while keeping a narrow kerf
 - Several Alignment Options
 - Special Coating material for better adhesion to prevent damage to the package
- These processes have allowed users to have smaller dicing widths and higher throughputs than Blade Dicing.
- In sum, Laser provide a good alternative to mechanical saw for narrow wide dicing $< 20 \mu\text{m}$.

Thank you for the Invitation & Listening!