

PELS Regional Lecturers Program

The PELS Regional Lecturers Program is a new initiative of the Power Electronics Society in order to continue supporting and recognizing all the technical activities the chapters develop to promote power electronics in their respective locations. This initiative recognizes that in many chapters there are individuals with locally and internationally recognized expertise in their fields, who have volunteered to give technical talks within their chapter or surrounding areas. The purpose is to help chapters exchange speakers to help broaden their programs. Please notice that this initiative is *in addition* to the Society Distinguished Lecturer Program. The difference is that the travel scope is more limited for the Regional Lecturers Program, so that costs can be borne directly by the inviting chapters.

The individuals selected by PELS chapters to be part of this program are listed below with their contact information and titles or topics of the talks they are offering. If you are interested in this program, just contact them, the corresponding [Chapter Chair](#), or the [PELS Chapter Development Chair](#) to make arrangements.

Spain

Prof. Juan Peracaula

Universidad Politécnica de Catalunya/ Barcelona

E-mail: peracaula@eel.upc.es

Topic: *"Field Oriented Control and Motor Drives-Solar Energy Converters"*

Prof. Luis Martinez-Salamero

Universidad "Rovira i Virgili"-Tarragona-Spain, ETSI-Escuela Tecnica Superior de Ingenieria

E-mail: lmartine@etse.urv.es

Topic: *"Control of DC/DC Converters-Adaptive and non-linear Control"*

Prof. Javier Uceda

Universidad Politécnica de Madrid

E-mail: juceda@vi.upm.es

Topic: *"Power Electronics and Magnetic Systems Design"*

Prof. Javier Sebastian

Universidad de Oviedo, Escuela Superior de Ingenieros de Gijón, Departamento de Electrónica

E-mail: sebas@ate.uniovi.es

Topic: *"DC/DC Converters Design and efficiency optimisation.PF Compensation"*

Prof .Enrique Dede

Universidad de Valencia / G.H. HELIN, S.A.

E-mail: enrique.dede@uv.es

Topic: *"High Power Converters for Industry Applications"*

France

Antoine Capel (Alcatel Espace, Toulouse, France) *

E-mail: Antoine.Capel@space.alcatel.fr

Topic: *"Power Electronics for Space applications"*

* He is also PELS Distinguished Lecturer

Prof. Gérard-André Capolino (University of Picardie, Amiens, France)

E-mail: Gerard.Capolino@sc.u-picardie.fr

Topic: *"Diagnostic techniques for motor drives"*

Dr. Claude Marchand (University Paris 11, Paris, France)

E-mail: Marchand@lqep.supelec.fr

Topic: *"Clean power supplies"*

Morelos

Jaime Arau (CENIDET, Cuernavaca, Mexico)

E-mail: jarau@cenidet.edu.mx

Topic: *"Introduction to Power Electronics", "Power Quality Trends"*

Maria Cotorogea (CENIDET, Cuernavaca, Mexico)

E-mail: pemcp@cenidet.edu.mx

Topic: *"Modeling of Power Semiconductor Devices"*

Mario Ponce (CENIDET, Cuernavaca, Mexico)

E-mail: ponce@cenidet.edu.mx

Topic: *"New Trends in HID Electronic Ballasts"*

Los Angeles Council

Robert D. Middlebrook (Caltech, retired)

Topic:

Bob Mammano (Unitrode)

Topic:

Chuck Mullet (Condor DC Power Supplies)

E-mail: chuck_mullett@condordc.com

Topic: *"Using MagAmp as a post regulation device"*

Bob Gourley (RDG Engineering)

E-mail: rdg_eng@earthlink.net

Topic:

Jerry Foutz (SMPS Technology)

E-mail: j.foutz@ieee.org

Topic: *"The World-Wide Web as a Power Electronics Design Resource"*

F. Dong Tan (TRW)

E-mail: dong.tan@trw.com

Topic: *"The forward converter: form the classic to the contemporary"*

Steve Jensen (Steve Jensen Consultants)

Topic:

Russia

Yuriy Rozanov (Moscow Power Engineering Institute- Technical University)

E-mail: y.rozanov@mtu-net.ru

Topic: *"Power Electronics", "Power Quality and improving of it by power electronic means"*

Felix Kovalev (Academy of Electrical Sciences, Moscow)

E-mail: apee@apmsun.mpei.ac.ru

Topic: *"Power Electronic Converters. Theory, design and applications"*

Stanislav Florentcev (Academy of Electrical Sciences, Moscow)

E-mail: apee@apmsun.mpei.ac.ru

Topic: *" Power semiconductor modules" (structures, designing, electrical isolation, producing)"*