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DSTO's Passive Radar Research

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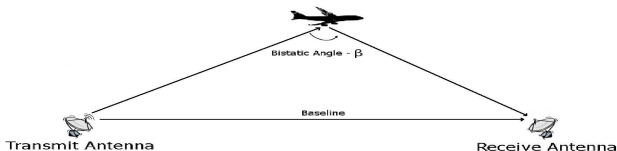
Defence Science and Technology Organisation of Australia

17th September, 2014

The Pros and Cons - Standard Bistatic Radar



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• Advantages

- Passive receiver
 - No RF emissions at RX
 - Covert surveillance
 - Difficult to jam
- Cheaper (?)
- Location

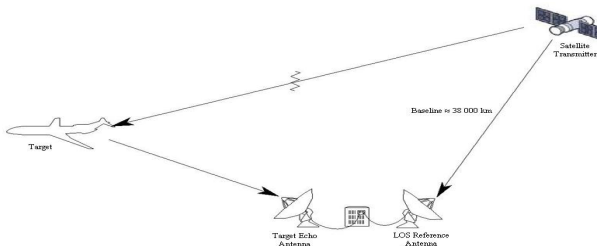
• Disadvantages

- Increased complexity
 - Geometry
 - System (Coherence issues)
- Decreased detection range
- Pulse Chasing
- Can exploit dedicated/cooperative transmitter(s) only

The Pros and Cons - Passive Radar



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Advantages

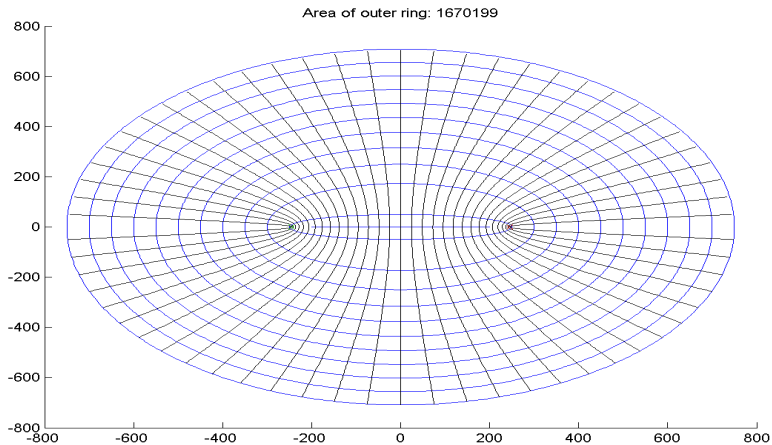
- Passive system
 - No new RF emissions
 - Covert surveillance
 - Very difficult to jam
- Cheaper (no new Transmitter)
- CW transmission (?)
- Multitude of Signal Sources:
 - TV, radio, cell phones
 - Satellites
 - ...

Disadvantages

- Increased complexity
 - Geometry
 - System (Coherence issues)
- Dependence on Transmitter
 - Waveform
 - Location & Coverage (Spatial & Temporal)
 - Transmitter power
 - Bandwidth

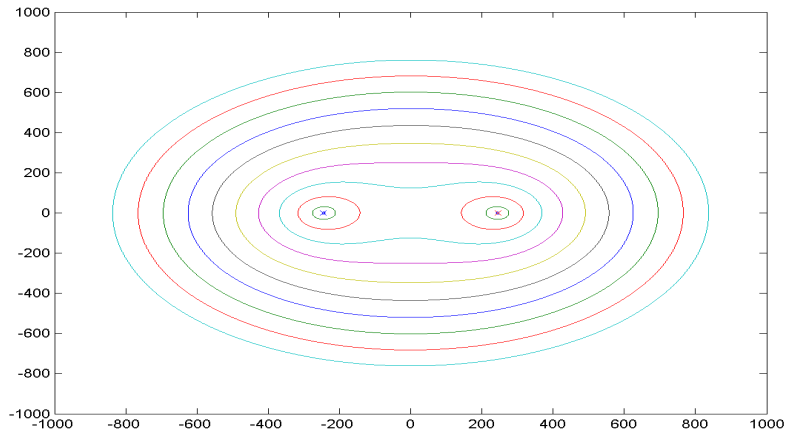
Baseline's Impact on Geometry

Range and Doppler Resolution



Baseline's Impact on Geometry

Signal-to-Noise Ratio

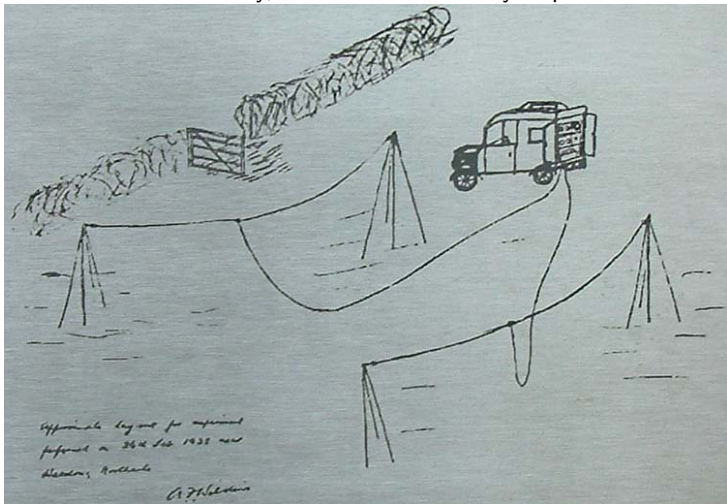


Passive Radar's History



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26th of February, 1935 - The Daventry Experiment



http://en.wikipedia.org/wiki/File:Daventry_expt.jpg

Pressure from all around - Spectrum



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Australian Spectrum Allocation

Frequency Band	ADF dedicated	Shared	Not available to ADF
29.7 MHz - 312 MHz	7.99%	28.25%	63.76%
312 MHz - 3.1 GHz	1.58%	8.21%	90.21%
3.1 GHz - 31 GHz	11.33%	21.38%	67.29%

The Underlying Technology



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Bistatic real-time processing is now fairly straight-forward

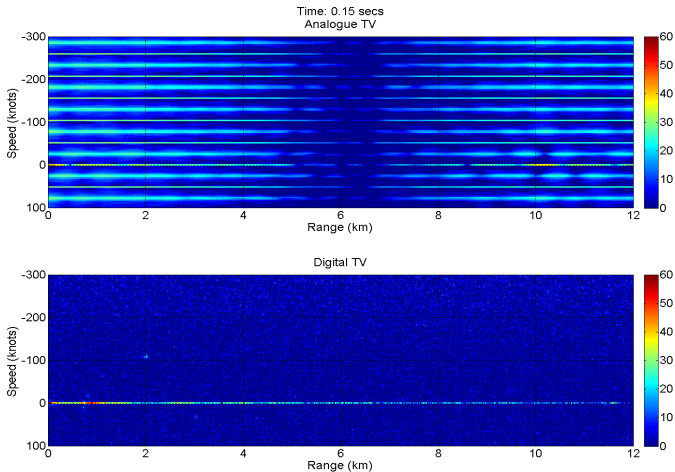
- In 2005 Howland demonstrated 'real-time' target detection using a 100kHz wide signal (FM radio)
 - Required 6 x 2.6GHz Pentium-4 PCs running in parallel
 - Only able to process 1s worth of data every 5s
- At DSTO we have a real-time demonstration system too
 - 8MHz wide signal (DVB-T - 80x more bandwidth than FM radio)
 - On a single i7 PC (circa March 2009)

Waveforms



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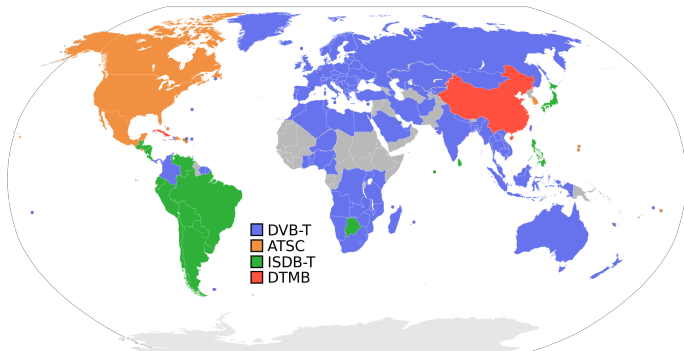
Real data showing Analogue TV and Digital TV
range Doppler maps with an airborne target present...



Digital TV around the World (as at May 2014)



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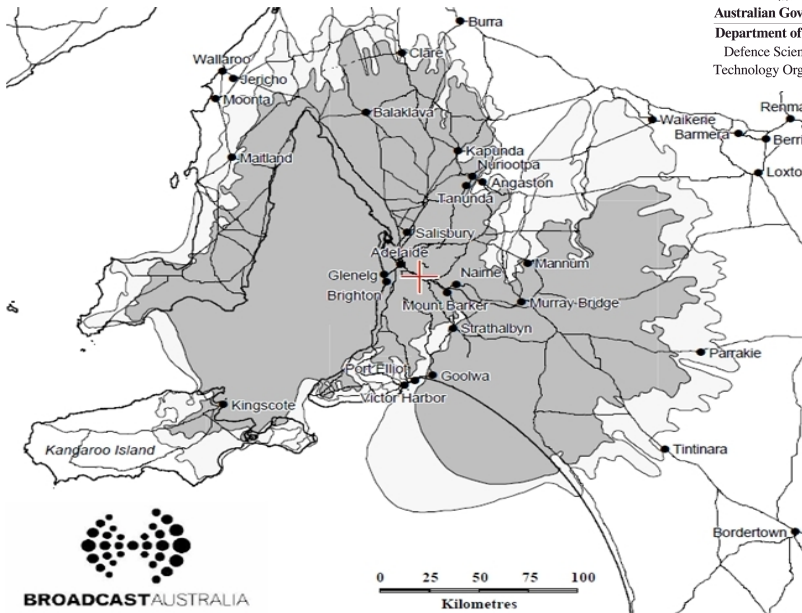


http://en.wikipedia.org/wiki/File:Digital_broadcast_standards.svg

DVB-T Coverage Map



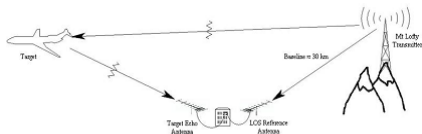
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DVB-T Experiments

- Antenna pointed at reference source
- Antenna pointed in direction of interest



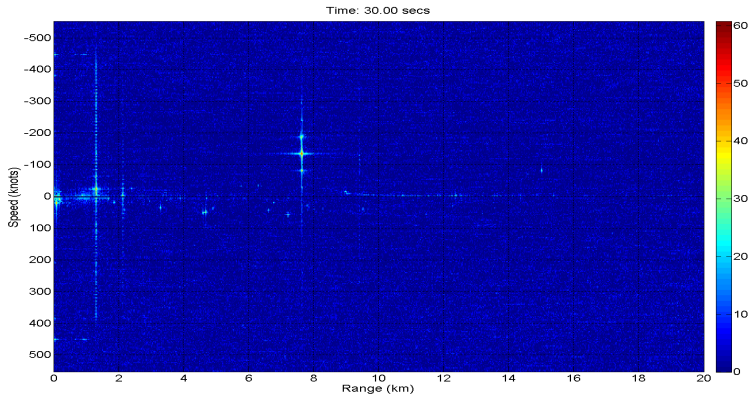
- All COTS hardware:
 - Readily Available receivers (typ. non-ITAR)
 - Domestic grade kit
- Cost for 2 Channel System: < \$60k



2008: DVB-T Performance - Receiver 30 km from Transmitter



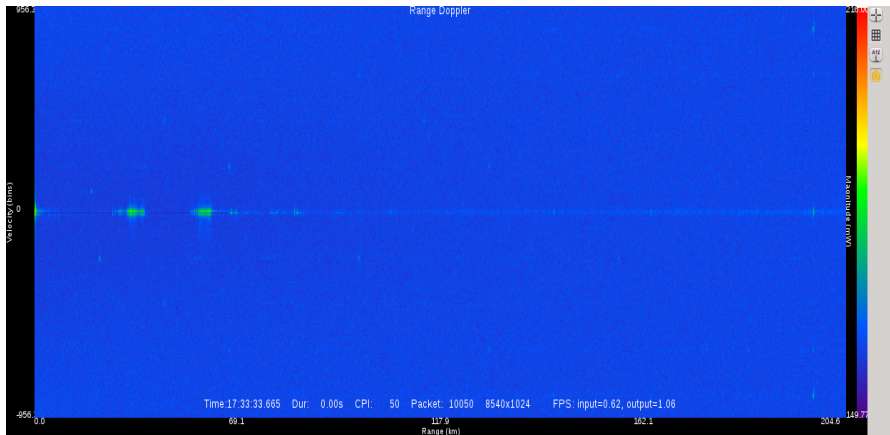
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DVB-T Performance - Receiver 88km from Transmitter - 1



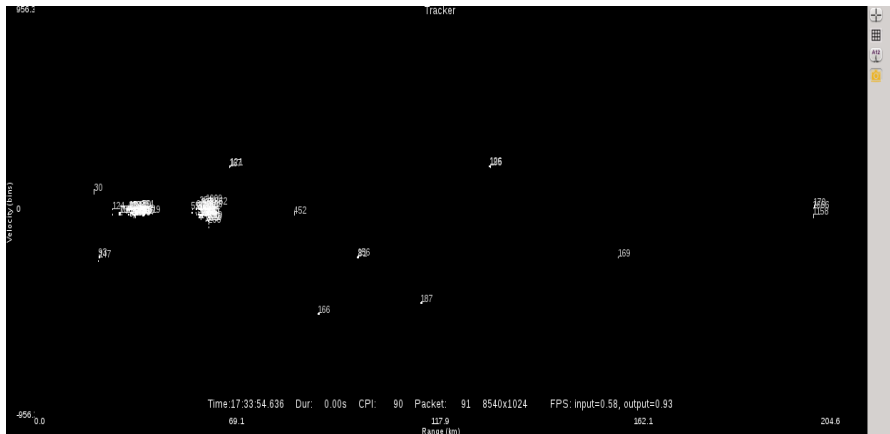
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DVB-T Performance - Receiver 88km from Transmitter - 2



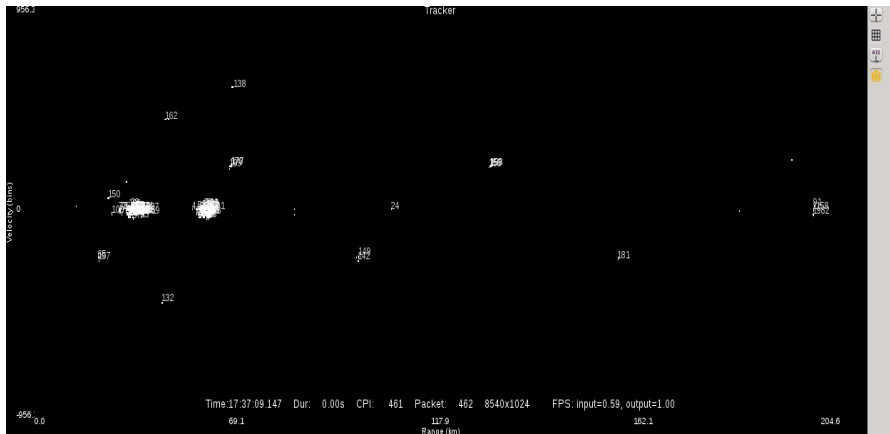
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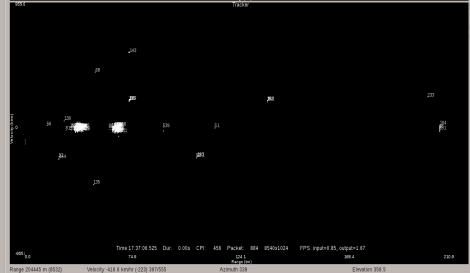
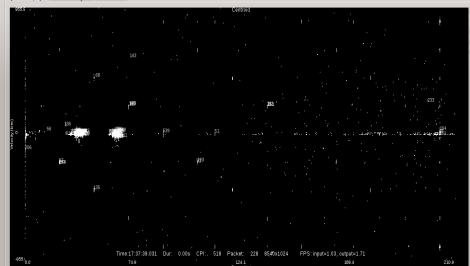


DVB-T Performance - Receiver 88km from Transmitter - 3



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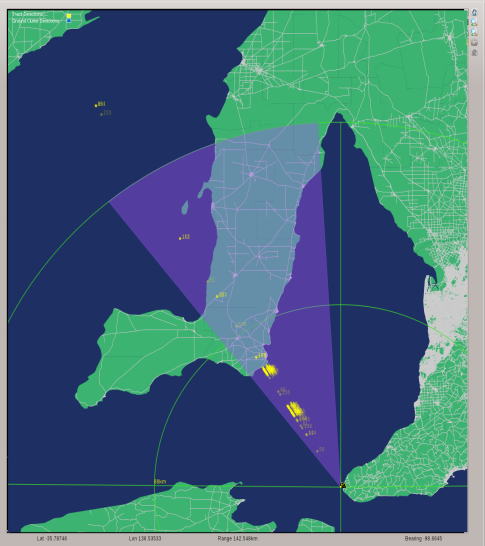




Range Processor

Range (m): 140 Speed (km/h): 769 CPI (s): 0.524268 CPI (FPS): 1.9

File Name	Time	Fx (MHz)	Fy (MHz)	Duration (s)	Host	AZE	GPS
./data/Caps/monrslg2_bmd_gy_09m1_854_00_00.dat	2012-12-04 17:34:07	0	854.5	30.4	python		
./data/Caps/monrslg2_bmd_gy_09m1_854_00_30.dat	2012-12-04 17:35:07	0	854.5	30.1	python		
./data/Caps/monrslg2_bmd_gy_09m1_854_01_00.dat	2012-12-04 17:36:07	0	854.5	16.5	python		
./data/Caps/monrslg2_bmd_gy_09m1_854_01.dat	2012-12-04 17:37:07	0	854.5	12.61 A	python		
./data/Caps/monrslg2_bmd_gy_09m1_854_06_00_29.dat	2012-12-04 17:38:07	0	854.5	29.9	python		
./data/Caps/monrslg2_bmd_gy_09m1_854_08.dat	2012-12-04 18:15:31	0	854.5	2811.9	python		
./data/Caps/monrslg2_ksnrg_027.dat	2012-12-04 18:40:27	31.25	1278.0	32.9	python		
./data/Caps/monrslg2_ksnrg_035.dat	2012-12-04 18:37:22	31.25	1278.0	51.2	python		
./data/Caps/monrslg2_ksnrg_051.dat	2012-11-08 18:50:54	31.25	1278.0	131.6	python		

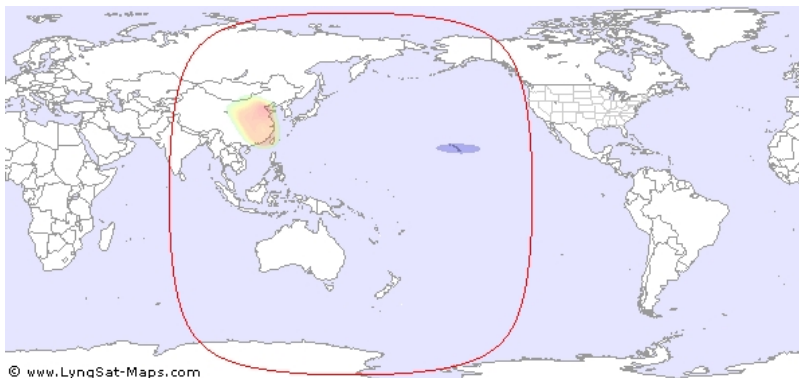
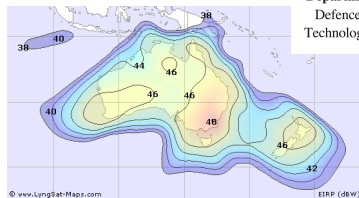
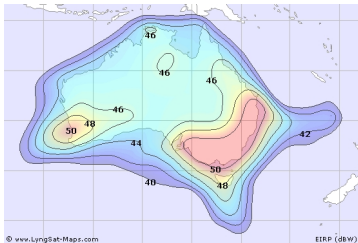


W. A.	Latitude (°)	Longitude (°)	Altitude (ft)	Range (ft)	Velocity (km/h)	Power (dB)	SNR (dB)	Strategic Angle (°)	CPI	Ext. Range (ft)
1995	35.3451	137.890	785.168	14313.7	0.11652	171.74	24.67	83.3869	457	
1992	35.3195	137.872	834.613	17685.5	25.710	171.74	24.67	78.2214	457	
1998	35.1424	137.725	1237.6	61253.8	39.8564	171.74	24.67	64.3033	457	
1698	35.1524	137.743	1243.67	19758.7	25.710	171.74	24.67	85.1841	457	
1611	35.1376	137.73	1276.03	61089.9	25.5263	174.48	37.42	64.8971	457	
1463	35.3113	137.865	856.490	18779.5	0.11652	193.89	48.82	77.536	457	
1222	35.1876	137.745	1232.08	14071.3	0.11652	168.08	28.96	85.5208	457	
977	35.3235	137.874	828.822	17425.3	12.7031	193.99	38.83	79.3869	457	
639	35.3955	135.854	736.165	14202.4	2.4661	138.45	10.45	74.2923	457	

DVB-S Coverage Map (Optus C1)



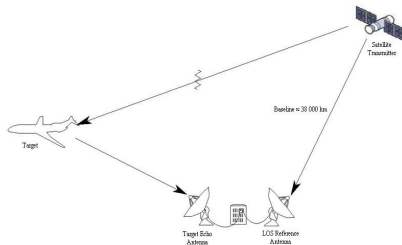
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DVB-S Experiments

- Antenna pointed at reference source
- Antenna pointed in direction of interest
- All COTS hardware
 - Domestic grade kit
- Replacement cost of ~\$70k

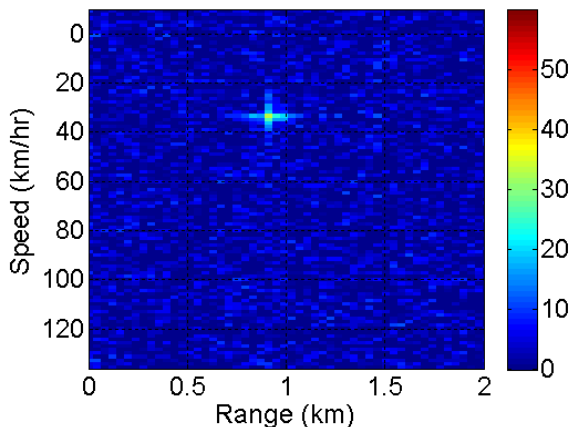


2008: Geo-sat based aircraft detection



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Time: 17.28 secs



2012: Geo-sat based people detection



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Direct Path Interference and Clutter



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- DPI is a big issue for DVB-T¹:
 - Thumbtack ambiguity constrains available “detection” dynamic range
 - Average peak-to-sidelobe ratio: $10 * \log_{10}(B * T_{cpi})$
- A number of mitigation approaches are available, including:
 - Analogue beamforming
 - Digital beamforming
 - Polarisation diversity
 - Digital filters, including:
 - LMS (inc. NLMS and Fast LMS (or block LMS))
 - RLS (inc. EDS and Fast EDS)
 - Wiener filter
 - Conjugate Gradient
 - Steepest Descent
 - Other DSP techniques
 - E.g. The CLEAN technique

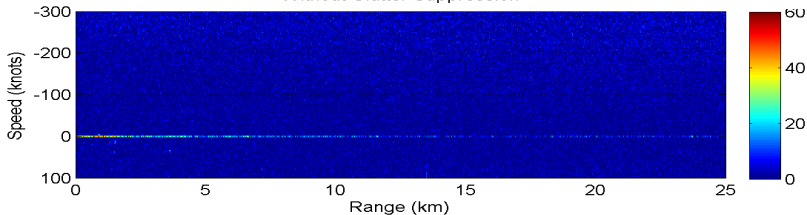
¹Less of an issue for DVB-S due to high directivity of antennas

Before and After application of Wiener Filter

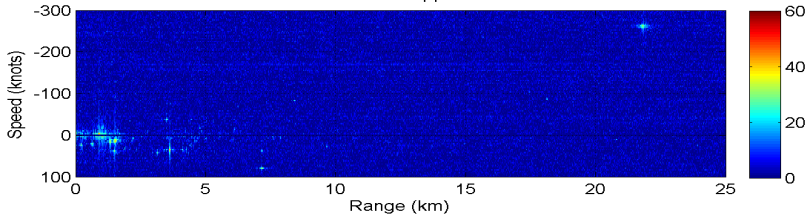


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Time: 4.95 secs
Without Clutter Suppression



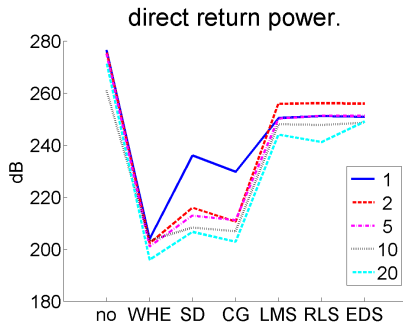
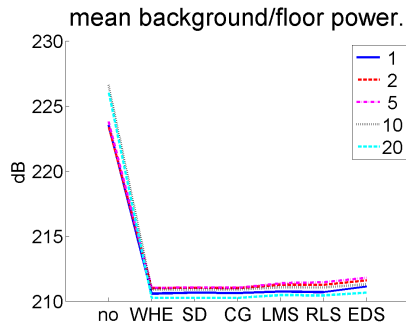
With Clutter Suppression





Results: removal of DPI only

Filter length $M = 100$

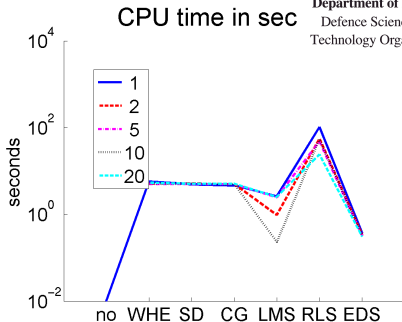
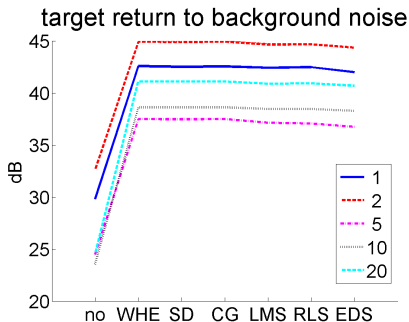


Results: removal of DPI only

Filter length $M = 100$



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- All methods reduce noise floor ~ 15 dB
- All methods increase target power/noise ~ 12 dB
- SD and CG approach same DPI suppression of WHE
- LMS, RLS, EDS achieve partial mitigation of DPI
- LMS and EDS require least CPU time
- SD & CG use same CPU time as WHE

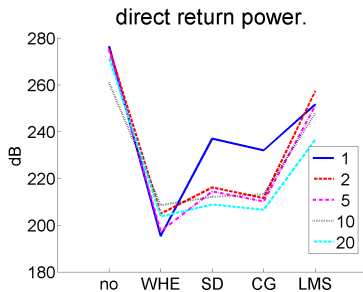
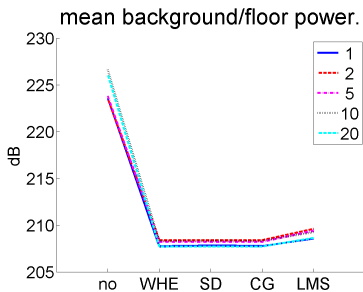
Results: removal of DPI and ZDC



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Filter length $M = 2917$.

RLS and EDS not used due to prohibitively large runtime.



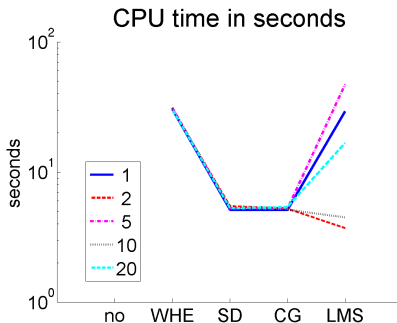
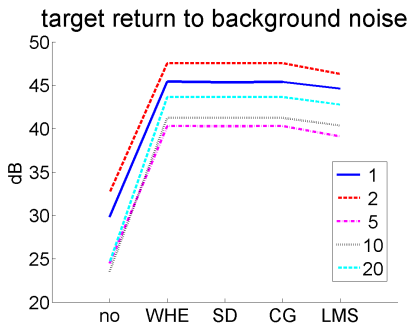
Results: removal of DPI and ZDC



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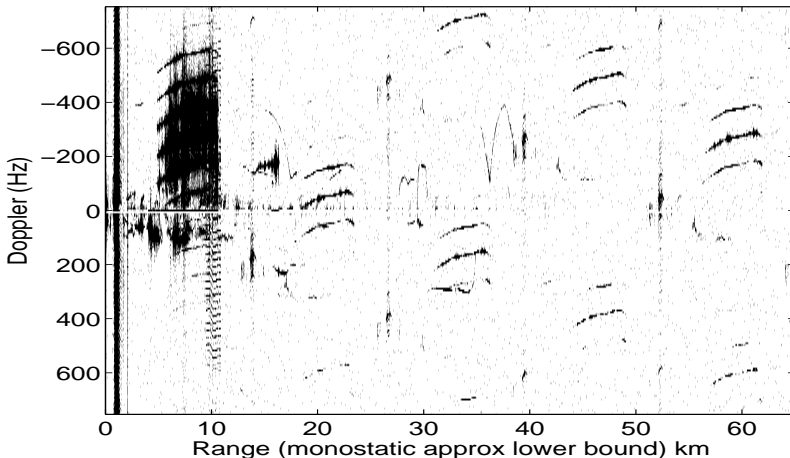
- CG suppresses 1–3 dB more ZDC than SD
- SD & CG requires consistently less CPU time than WHE
- CPUtime required by LMS highly variable.

Demod / Remod - Multipath mitigation, noise reduction and ambiguity control



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CFAR Detection History - *Before* and *After*...

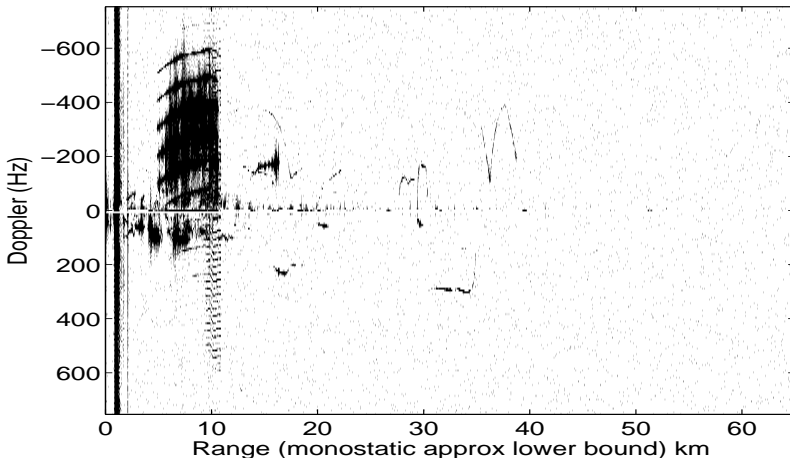


Demod / Remod - Multipath mitigation, noise reduction and ambiguity control



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CFAR Detection History - Before and *After*...



Processing time constraints - CPI



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To achieve real-time performance:

- **Ideal: Finish all processing calculations in less time than a CPI!** This includes:
 - DPI and clutter suppression
 - RD map formation
 - Target detection
 - Target tracking*
 - Geolocation*
 - Display*

System Overview



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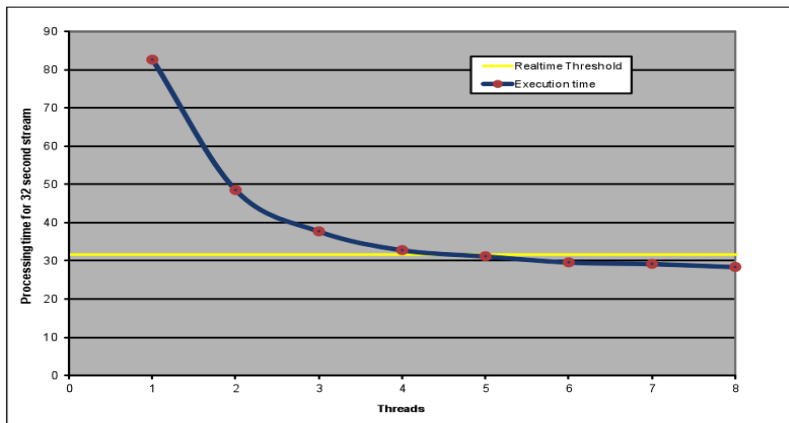
- Our computing platform (circa 2012) consists of:
 - a multi-core Intel processor (Core i7 x980 @ 3.3GHz CPU)
 - OpenSuse 11.4 Linux
 - 24GB RAM
 - Either a GTX285 or a Tesla C2075 NVIDIA GPU card
- Using one core (non-parallel implementation)
 - More than 2x slower than realtime

Parallel CPU Results - no clutter suppression

- Achieves realtime with 6 threads
- Performance is worse if range/Doppler increased
- Shortcomings:
 - Many cores of CPU in use
 - No capacity for other processing



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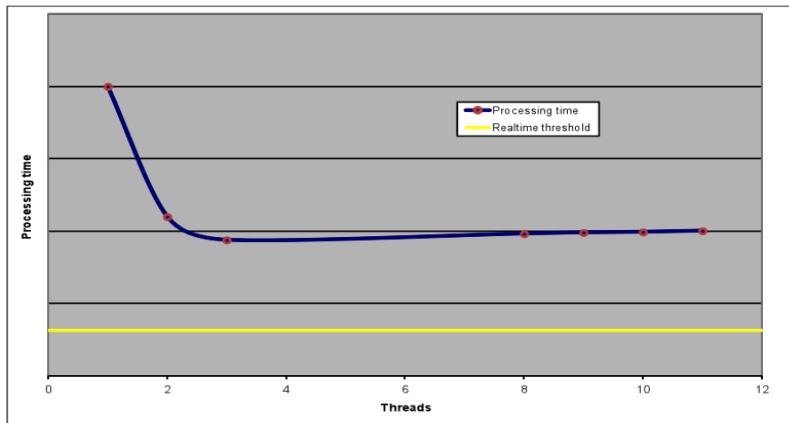


Parallel CPU Results - with clutter suppression



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- Single threaded processing now 6 times slower than realtime
- Because clutter filtering works on entire CPI time series:
 - Filter not as easily parallelised - no parallel CPU FFT at the time
 - This is a bottleneck at the input
 - Manifestation of Amdahls law



Reasons for non-scalability



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- We moved to CUDA and achieved realtime
 - So no business case for in-depth analysis
- Speculation as to likely reasons includes:
 - Thread/IPC overhead
 - Cache overflow effects
 - Amdahl's law:

“The speedup of a program using multiple processors in parallel computing is limited by the time needed for the sequential fraction of the program²”

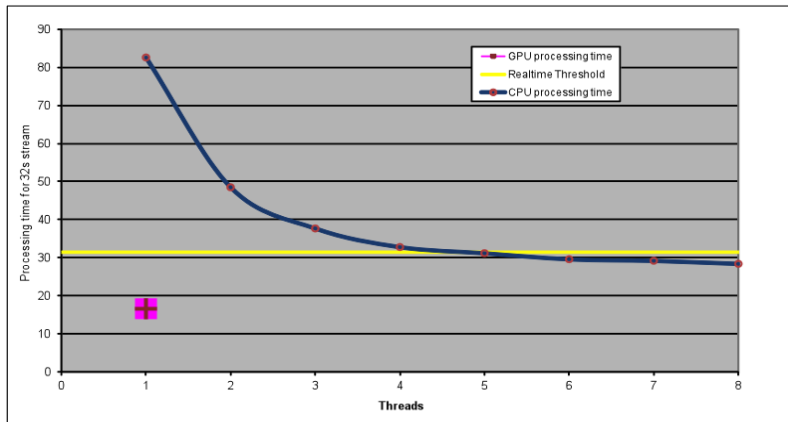
²http://en.wikipedia.org/wiki/Amdahl's_Law

GPU Results - no clutter suppression



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- Achieve realtime in one thread
- Saves four cores for other processing

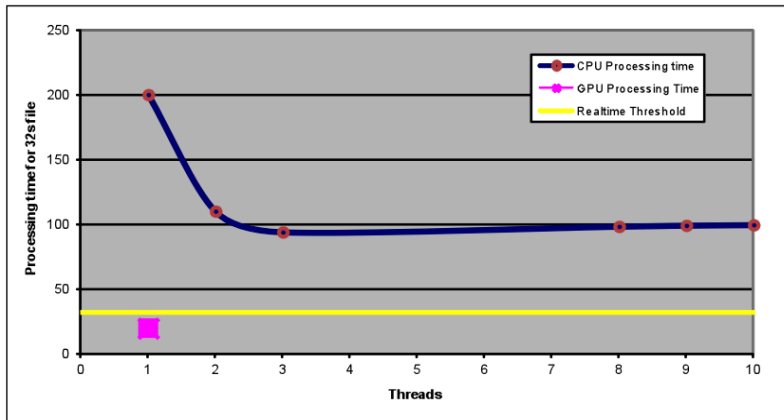


GPU Results - with clutter suppression



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- Achieve realtime in one thread
 - Clutter uses large FFTs which can be parallelised in GPU
 - Was not possible in CPU for given configuration



Where to next?



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- CTD - bigger, better, faster, more...
- Phased Array NPP - multi-element arrays for wide field of view and large effective aperture
- Understanding the practical limits of processing and hardware
- Investigate significant unknowns:
 - Bistatic RCS
 - Bistatic Clutter
 - Propagation effects

Acknowledgements:



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A *lot* of people have contributed to this work over the years. Special thanks go to:

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