IEEE · **Sixth IEEE Dallas Circuits and Systems Workshop** · **SMU**

November 15 – 16, 2007
Southern Methodist University, Dallas, TX

System-on-Chip (SoC): Design, Application, Integration and Software

The IEEE Circuits and System Society Dallas Section is conducting its sixth workshop (DCAS-07) to provide a forum for sharing design, application, integration and software aspects of System-on-Chip (SoC).

DCAS-07 is sponsored by IEEE Circuits and System Society Dallas Section and will be held on Nov. 15-16 on the campus of Southern Methodist University, Dallas TX

The technical program committee invites researchers from academia and industry to contribute new and previously unpublished results in the following Circuits and Systems related areas:

- High Performance and Low Power Circuits
- Digital Signal Processors and Cores
- Baseband Communication Processors
- RF Processors and Circuits
- Multimedia Processors
- Reconfigurable Processors
- Low Voltage and Mixed Signal Circuits
- Mixed Signal Integration

- A/D and D/A Conversion
- High Speed I/O
- Clocking and Clock Distribution
- Power Management Circuits
- Signal Integrity and On-chip Interconnects
- SoC Implementation Methodology
- SoC Verification
- SoC and IP integration

Paper Submission Details

Submission deadline: now extended to 08/31/2007
Acceptance notice: 09/28/2007
Camera ready draft due: 10/12/2007

Web-based, automated paper submissions (not to exceed 4 pages), review and notification process
http://www.ewh.ieee.org/soc/cas/dallas/wks2007

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