

## **CPMT Society News:**

### **CALL FOR CANDIDATES**

The CPMT Society is governed by a Board of Governors composed of officers, 18 elected members-at-large, and various committee chairs and representatives (see inside cover of this Newsletter for details.)

Annually, Society members are asked to elect six members-at-large for a three-year term of office. Candidates for member-at-large are selected in two ways -- either by the Society Nominating Committee, or by petition.

This year's election is the fourth in which members-at-large will be elected to achieve totals proportionate to the geographic distribution of CPMT members. Voting members will elect members-at-large from within their Region only (that is, members in Region 8 will vote for members-at-large from Region 8, etc.)

Elected Members of the Board of Governors must be willing to attend two annual Board meetings and participate actively in areas of their interest (publications, conferences, membership development, chapter development, etc.) The term of office for this election is 1 January 2010 through 31 December 2012.

If you are an IEEE and CPMT Society member in good standing and are interested in serving on the Board of Governors, you can become a candidate via petition by following the procedures below.

- Prepare a petition that contains your name, member number, and statement of your qualifications for office.
- Provide lines for signatories. Each line should include space for a printed name, member number, and signature.
- Have the petition signed by a MINIMUM of 46 CPMT Society members in good standing (Student grade members are not eligible to sign.)
- Submit your petition by mail no later than Friday, 25 July 2008 to:

CPMT Society Nominations Committee  
c/o Marsha Tickman  
IEEE CPMT Society Executive Office  
445 Hoes Lane, PO Box 1331  
Piscataway, NJ 08855-1331 USA

OR

- Request establishment of electronic petition process, allowing signatures to be collected on-line.

You must contact Marsha Tickman to implement electronic petition process.

Membership status of all signatories will be validated. It is suggested that you gather more than 46 signatures in order to assure meeting the minimum required number of valid signatures.

If you have questions or need additional information, contact Marsha Tickman at the above address, by phone at 732-562-5529, or by e-mail at [m.tickman@ieee.org](mailto:m.tickman@ieee.org).

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### **Congratulations to IEEE CPMT Fellows Class of 2009**

The grade of Fellow recognizes unusual distinction in the profession and shall be conferred by the Board of Directors upon a person with an extraordinary record of accomplishments in any of the IEEE fields of interest. (Bylaw I-104:11) Nominees shall:

- have accomplishments that have contributed importantly to the advancement or application of engineering, science and technology, bringing the realization of significant value to society;
- hold Senior Member or Life Senior Member grade at the time the nomination is submitted;
- have been a member in good standing in any grade for a period of five years or more preceding 1 January of the year of elevation.

The year of elevation to the grade of Fellow is the year following approval by the Board of Directors conferring the grade of Fellow. Members elevated to the Fellow grade may use the title immediately following approval by the Board of Directors.

The IEEE Fellows, an elite global group with international recognition are called upon for guidance and leadership as the world of electrical and electronic technology, continues to evolve.

#### **Moises Cases**

USA

*for contributions to design and noise control for power and signal distribution in digital systems*

#### **Shen-Li Fu**

Taiwan

*for contributions to electronic packaging research and education*

#### **Yoshitaka Fukuoka**

Japan

*for development of multichip module and high density substrate technologies*

#### **Lih-Tyng Hwang**

USA

*for contributions to System-in-a-Package technologies*

#### **Sung Kang**

USA

*for contributions to lead-free solders in microelectronics*