16th IEEE Workshop on Signal and Power Integrity

Supported by
CREATE Consortium
Naples

Title history: (1997-2011) Workshop on Signal Propagation on Interconnects

13-16 MAY 2012, SORRENTO (ITALY)

IEEE CPMT EMC SOCIETY

— Sponsored by IEEE Components, Packaging and Manufacturing Technology Society
— Technically co-sponsored by IEEE Electromagnetic Compatibility Society

MeMo

website, www.spi2012.org
run, 13-16 May, 2012
venue,
Grand Hotel Parco dei Principi, Sorrento, Italy

deadlines,
Submission of manuscripts:
January 31st, 2012
Notification of acceptance:
February 28th, 2012

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CALL FOR PAPERS

During the last fifteen years, this Workshop on Signal Propagation on Interconnects has been developed into a forum of exchange where world class developers and researchers will share and discuss leading edge results in the field of interconnect modeling, simulation and measurement at chip, board, and package level. The workshop is also meant to bring together developers and researchers from industry and academia.

In view of the last years success, the Committee is looking forward to the 16th Edition, convened at the Grand Hotel Parco dei Principi in Sorrento, with extraordinarily stunning views of the sea. The symposium will include both oral and poster sessions. A number of prominent experts will be giving tutorials on areas of emerging interest.

TOPICS

- Signal Integrity
- High-speed interconnects and high-speed channels
- Power Integrity/ Ground Noise
- Power Distribution Networks
- Electronic packages and Microsystems
- 3D technologies for ICs and packages
- RF, Microwave packaging and mixed signal systems
- Nano-Interconnects and nano-structures
- Electromagnetic Theory and Modeling
- Transmission Line Theory and Modeling
- Macro-Modeling, reduced-order models
- Advanced Simulation Tools for Signal and Power Integrity
- Electromagnetic Compatibility
- Coupling Effects on Interconnects
- Radiation & Interference
- Testing & Interconnects
- Time and Frequency Domain Measurement Techniques

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