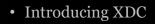


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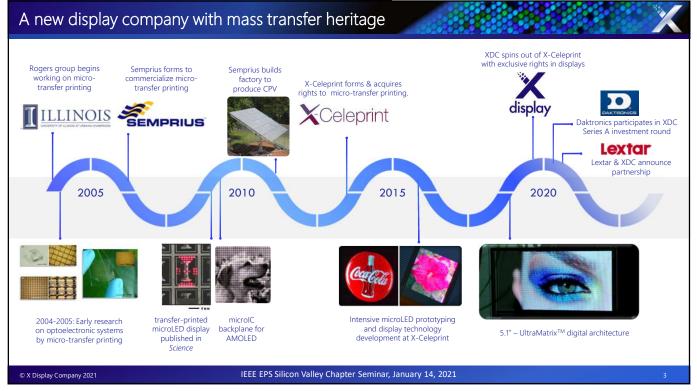


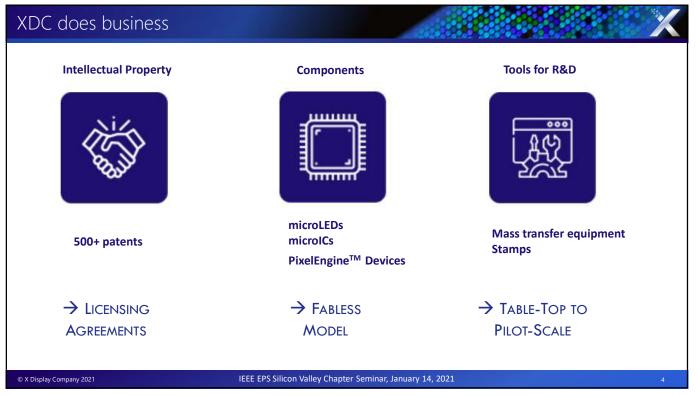


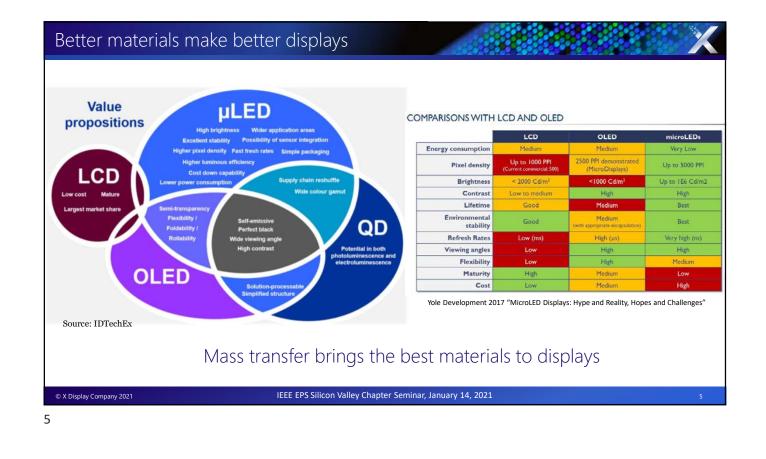
- Elastomer stamp mass transfer
- Mass transfer for displays

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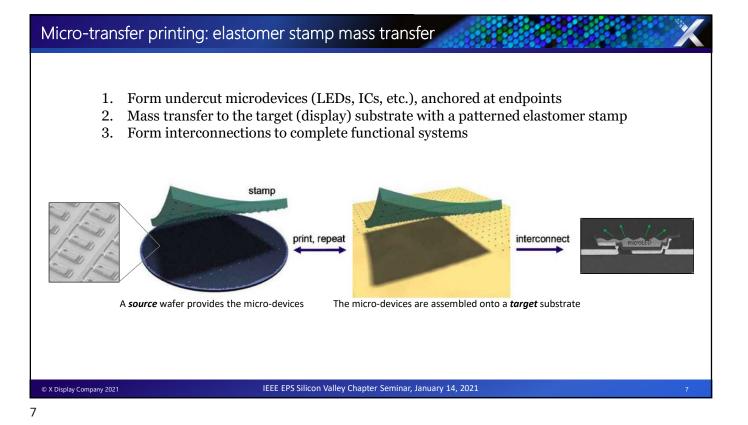
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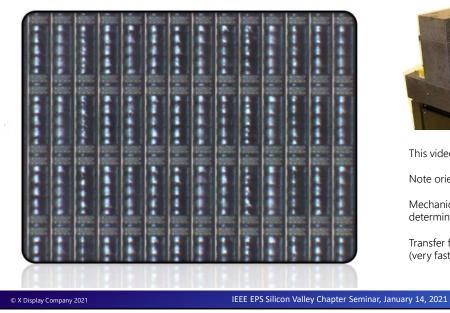


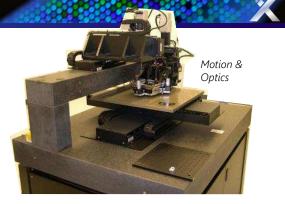


Elastomer stamp mass-transfer fundamentals stiff GLASS PDMS ELASTOMER compliant Elastomer stamp performs mass transfer by selectively retrieving an array of devices by van der Waals' adhesion and transferring the array to a display substrate (e.g. glass or plastic). Throughput (UPH) determined by array 4 9s (99.99%) yield proven in R&D lab. Expect 6 9s or higher in production. pitch, stamp size, and cycle time. 10000 2M posts 1000 Million Units per Hour **STAMP CHARACTERISTICS:** 260k 100 Key enablers for • compliant in z-direction 65k yield and throughput short-range, rate-tunable adhesion 12k posts 10 transparent ٠ 1600 posts • low-cost 0.1 mechanically tough 0 100 200 300 400 500 600 700 Stamp cycles per Hour © X Display Company 2021 IEEE EPS Silicon Valley Chapter Seminar, January 14, 2021

Mass-transfer in action \rightarrow motion + optics

Looking through the stamp: Retrieve ICs with stamp, print to display, loop video.





This video shows 20 second cycle time.

Note orientational control (7 contact pads on IC).

Mechanical array alignment can define the rate of deterministic mass-transfer micro-assembly.

Transfer forces act only for a few seconds of the cycle (very fast).



Transfer-printer designed to populate 300mm diameter wafers

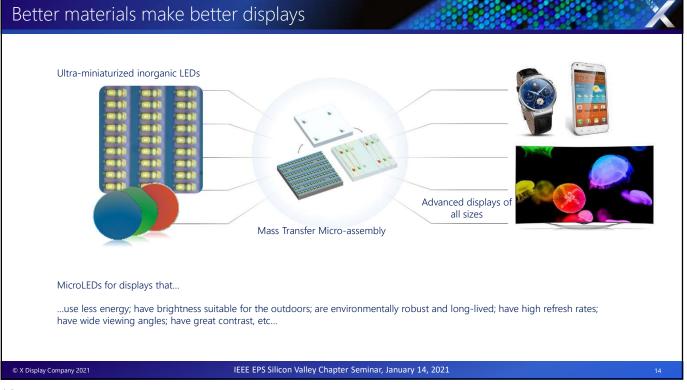
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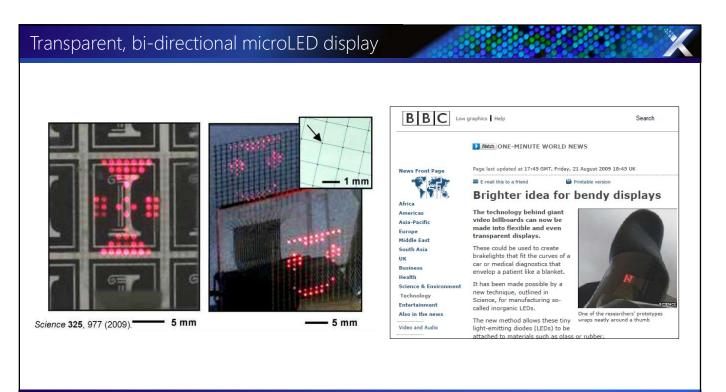
Capability demonstrations



Capability demonstrations stamp lifetime studies $3 \times 3 \ \mu m$ chips printed at 60um pitch > 30,000 transfers without performance degradation A GaN Microstructures: "Anchor" "Tethe N C Si (1 1 1) 00 0006 17000 00 00061 İşşəişti İli İli talı talı talışı talı 3000 5000 0007 13000 15000 19000 19000 13000 23000 1000 7000 © X Display Company 2021 IEEE EPS Silicon Valley Chapter Seminar, January 14, 2021

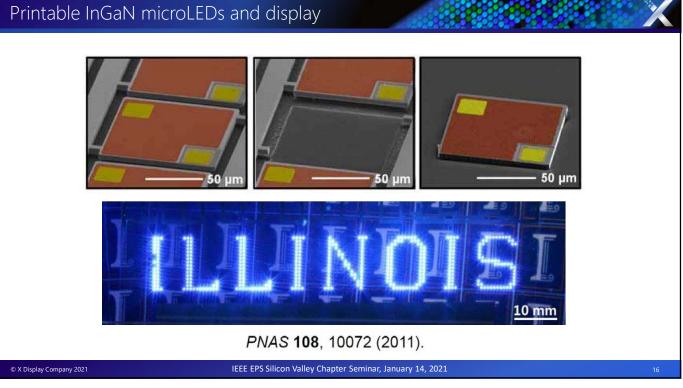




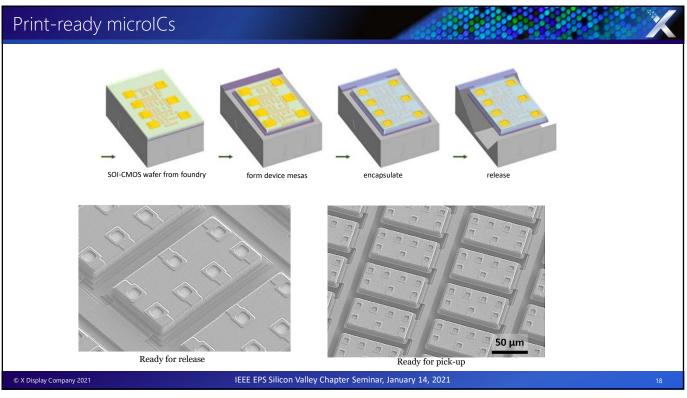


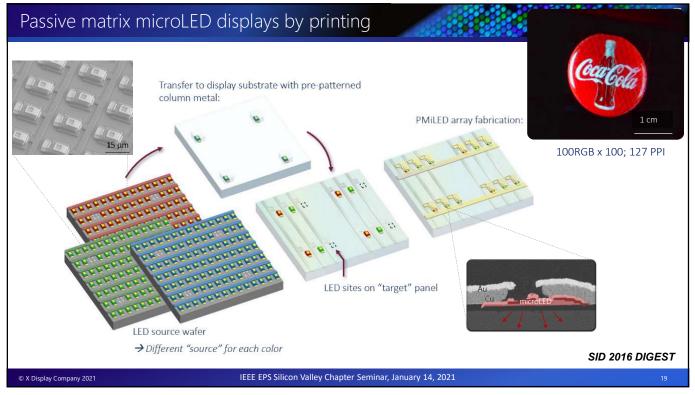
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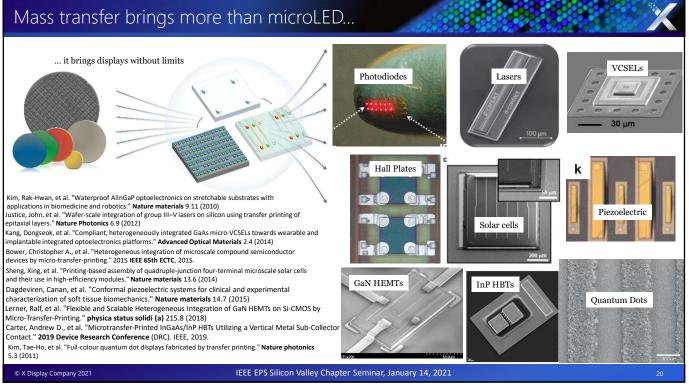
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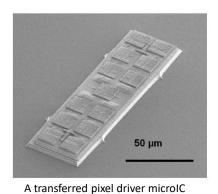


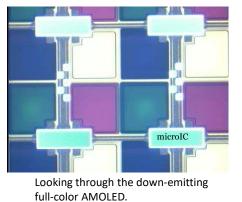


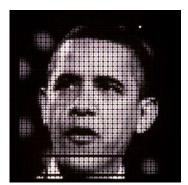


Mass transfer brings microICs to displays

Kodak and Semprius demonstrated AMOLED backplanes using mass transferred microICs in the late 2000s.







November 2008

63.2 AMOLED Displays using Transfer-Printed Integrated Circuits, SID 2009

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