Electromagnetic Field Visualization System for IC/Package Design Based on Optical Techniques

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NFC

Background and motivation



It is highly desirable to prevent the problems in the early stages of design and packaging of electronic components.

Background and motivation

Conventional circuit performance evaluation/testing techniques such as a network analyzer and a TDR It is difficult to identify the concrete location of the faulty components inside the circuits. They cannot give direct information about an EM coupling position and individual decoupling capacitor performance.





Background and motivation



Development and evaluations of a near-field probe that utilizes optical measurement techniques.

FEMO/EO probing system



All apparatuses in the optical system are connected by optical fibers. **Free from optical alignment** The probe head consists of an optical fiber and a minute crystal. **Superior low-invasiveness High spatial resolution**

Development and application of high performance near-field probing system toward the high-speed and low-noise design of ICs/packages.

Magnetic near-field maps over fine meander circuits



Probe head and device under test (DUT)





DUT for near-field mappings



Frequency response of the FEMO probe



X-Z near-field maps



X-Z near-field map



Line/Space 20 µm

X-Y near-field maps



Verification of measured results by electromagnetic field simulator

Simulated maps of the amplitude of H_z at nearby the middle position of the circuit.



Electromagnetic near-field measurements over active devices

DUT for near- and far-fields evaluations



Radiated electric field strength



High-level emissions are observed at 12.75 x 2n MHz.

Magnetic near-field distributions over the LSI package pins



Frequency dependence of MO signals



Between power and ground pins: high-level signals are observed at 12.75 x 2n MHz (Same feature as the electric far-field characteristic)

This suggests that the origin of the high-level electric field radiation is a current flowing in the power supply system of the I/O circuits.

LSI chip for near-field measurements



10.4 mm

Power/ground line for logic circuits
 Width of horizontal lines : 1.2 μm
 Width of vertical lines : 10 μm

Magnetic/electric near-field maps over an LSI chip



Magnetic field distribution

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Electric field distribution

Magnetic field: strong intensity above the power supply system Electric field: strong intensity above the signal system This suggests that detail SI/PI analyses can be performed by the EO/MO probing technique. We introduced a wideband MO probe with a 10-µm-class spatial resolution.

GHz-region magnetic near-fields over a fine circuit were successfully mapped.

The potential of the probe for near-field characterization and EMI source evaluation of an active device was shown.

These results demonstrate that the FEMO/EO probing technique can be an effective tool toward an optimum design/packaging of electronic components. This work was performed under the management of ASET in the basic plan of Research and Development on Ultra High-Density Electronics System Integration supported by NEDO.

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