



Announcing the New EMC Society Respected Speakers Bureau

Bruce Archambeault, Technical Advisory Committee (TAC) Chair

Have you ever heard a speaker at a local EMC Society Chapter meeting, EMC symposium, or EMC Society “Tabletop” Seminar, and wished to hear more from him or her? The EMC Society has many speakers who are well known and respected and a great resource for local Chapters.

The EMC Society has established the Respected Speakers Bureau to provide local Chapters with a list of speakers that are well respected and are recommended to the Chapters as possible speakers (in addition those in the Distinguished Lecturer program). These speakers may be past Distinguished Lecturers or other well known and respected EMC professionals.

Travel for these speakers is not funded by the Society, but may be funded by local Chapters or may be in conjunction with other travel by the speaker (resulting in no extra travel costs). Angel funds may also be possible to help support this activity.

The Respected Speaker Bureau information may be found in the Distinguished Lecturer section of the IEEE EMC Society web site. The list of Respected Speakers is growing, so keep looking at the list for your favorite speaker!

The list of speakers in the Respected Speaker Bureau is not long yet. The initial speakers that have agreed to serve the Society in this manner are listed below. If you have any suggestions for additional speakers, please contact Bruce Archambeault at bruce.arch@ieee.org.

Colin Brench colin.brench@ieee.org

- EMI Shielding, Design and Measurements Methods for Product Development
- Antenna Behavior for EMC Engineers
- Measurements and Modeling – Two Very Similar Issues

Jim Drewniak drewniak@mst.edu

- Power Distribution Network Concepts and Design for Multi-layer PCBs
- High-Speed Link Path Analysis and Design – Dividing and Conquering
- Concepts for PCB EMI Design

Cheung-Wei Lam lam@apple.com

- Obtaining Benefits from EMC Software Analysis Tools
- Common Misconceptions about Inductance and Current Return Path
- Signal Integrity Design versus Radiated Emission Control

Werner Schaefer wsemc@cisco.com

- Measurement of Noise-like Signals with a Spectrum Analyzer or EMI Receiver
- Significance of EMI Receiver Specifications for Commercial EMI Compliance Testing
- Absorber Placement for Achievement of Free-Space Conditions - An Alternative Method for the Improvement of EMI Test Sites
- EMC Laboratory Accreditation in the US

David Pommerenke davidjp@mst.edu

- EMI Optimal Buck Converter PCB and Circuit Design

- Electrostatic Discharge – It failed! How to find the root cause!
- System ESD Simulation
- Near-field Scanning Techniques: Immunity, EMI and Resonance Analysis

Bruce Archambeault Bruce.arch@ieee.org

- Introduction to Fullwave Modeling and Tools for EMC Design
- The Ground Myth
- Decoupling Capacitor Strategy for Effective EMI and PI

Elya B. Joffe eb.joffe@ieee.org

- Path of Least Inductance
- Grounding
- EMC Troubleshooting and Diagnostics
- Cost of EMC

Jun Fan jfan@mst.edu

- Power Distribution Network Design for High-speed Printed Circuit Boards
- Crosstalk Among Vias
- Via Optimization for Signal Integrity
- EMI Fundamentals
- A Case Study with Embedded Capacitance

Chris Holloway christopher.holloway@nist.gov

- Overview and Basics of Reverberation Chambers and their Applications for EM Measurements
- Introduction, Overview, and Applications of Metamaterials and Metafilms
- Propagation and Detection of Radio Signals Before, During, and After the Implosion of Large Building Structures

EMC



PHOTO BY KEN WYATT

Kudos to the dynamic team of Dave and Barbara Staggs for their time and talent as Chairman and Companion Program Chair, respectively, for the 2009 IEEE International Symposium on EMC in Austin, Texas. The EMC Society appreciates your efforts and those of your excellent committee resulting in a very successful Symposium!