Brief Bio: Rajeev Jain is an Emeritus Professor in Electrical Engineering at UCLA and a Senior Director at Qualcomm Research. He is also a Fellow of the IEEE. Rajeev Jain received the B.Tech. degree in Electrical Engineering from the Indian Institute of Technology in 1978, and the Ph.D. degree in Electrical Engineering from the Katholieke Universiteit, Leuven, Belgium, in 1985.Dr. Jain worked at Siemens AG in Munich, Germany, where he developed CAD tools for automatic programmable signal processor code-generation to implement digital filters. He has also worked at IMEC, Leuven, where he was responsible for the development of the Cathedral-I CAD system for automatic design of DSP circuits using bit-serial architectures, which has resulted in the commercial product called DSP Station. As a research engineer at the University of California, Berkeley, he worked on the development of the Lager IV design system for bit-parallel signal processing circuits. At UCLA, Prof. Jain has created courses in DSP design and has pioneered R&D programs in the design of high-speed communication circuits and wireless multimedia networking systems.Professor Jain's research focuses on embedded hardware-software design for signal processing systems-on-a-chip. Current research efforts are concentrated on CAD tools for design of high-performance signal processing architectures and the development of ASICs for spread-spectrum modems and image compression. In 2011 he joined Qualcomm Research where he leads research in always-on context-aware computing using sensor fusion. His research team is developing context-aware technologies for smartphones and wearable devices by applying machine learning to a variety of sensory inputs including inertial, audio, video and radio signals.Awards and Recognitions include: 1999-IEEE Fellow; 1995- Allied Signal Faculty Research Award;1991-Young Faculty Award from Northrop Grumman Industries.